Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

Notice

- 1. All information included in this document is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any Renesas Electronics products listed herein, please confirm the latest product information with a Renesas Electronics sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by Renesas Electronics such as that disclosed through our website.
- Renesas Electronics does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
- 3. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part.
- 4. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
- 5. When exporting the products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations. You should not use Renesas Electronics products or the technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations.
- 6. Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
- 7. Renesas Electronics products are classified according to the following three quality grades: "Standard", "High Quality", and "Specific". The recommended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below. You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application categorized as "Specific" without the prior written consent of Renesas Electronics. Further, you may not use any Renesas Electronics. Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for an application categorized as "Specific" or for which the product is not intended where you have failed to obtain the prior written consent of Renesas Electronics. The quality grade of each Renesas Electronics product is "Standard" unless otherwise expressly specified in a Renesas Electronics data sheets or data books, etc.
 - "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots.
 - "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anticrime systems; safety equipment; and medical equipment not specifically designed for life support.
 - "Specific": Aircraft; aerospace equipment; submersible repeaters; nuclear reactor control systems; medical equipment or systems for life support (e.g. artificial life support devices or systems), surgical implantations, or healthcare intervention (e.g. excision, etc.), and any other applications or purposes that pose a direct threat to human life.
- 8. You should use the Renesas Electronics products described in this document within the range specified by Renesas Electronics, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. Renesas Electronics shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.
- 9. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
- 10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. Renesas Electronics assumes no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
- 11. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written consent of Renesas Electronics.
- 12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.
- (Note 1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its majorityowned subsidiaries.
- (Note 2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

Renesas

MOS FIELD EFFECT TRANSISTOR

μ PA2792AGR

SWITCHING N- AND P-CHANNEL POWER MOS FET

DESCRIPTION

The µPA2792AGR is N- and P-channel MOS Field Effect Transistors designed for Motor Drive application.

FEATURES

- Low on-state resistance
- N-channel $R_{DS(on)1} = 12.5 \text{ m}\Omega \text{ MAX}$. (Vgs = 10 V, ID = 5 A) $R_{DS(on)2} = 21 \text{ m}\Omega \text{ MAX.}$ (Vgs = 4.5 V, ID = 5 A) P-channel $R_{DS(on)1} = 18 \text{ m}\Omega \text{ MAX}$. (VGS = -10 V, ID = -5 A)

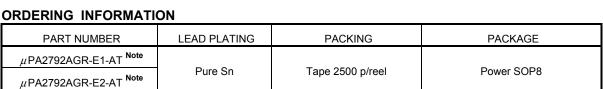
 $R_{DS(on)2} = 26 \text{ m}\Omega \text{ MAX.}$ (Vgs = -4.5 V, ID = -5 A)

Low input capacitance

N-channel Ciss = 2200 pF TYP.

- P-channel Ciss = 2200 pF TYP.
- Built-in gate protection diode
- Small and surface mount package (Power SOP8)

ORDERING INFORMATION



Note Pb-free (This product does not contain Pb in external electrode and other parts.)

EQUIVALENT CIRCUITS

N-channel P-channel Drain Drain Body Body Gate Diode Gate Diode Gate Gate Protection Protection Source Source Diode Diode

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

> The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version. Not all products and/or types are available in every country. Please check with an NEC Electronics sales representative for availability and additional information.

ĂННĂ : Source 1 N-channel 1 2 : Gate 1 7, 8: Drain 1 Source 2 P-channel 3 Gate 2 5, 6: Drain 2 ()Ħ F 6.0 ±0.3 4.4 5.37 MAX. 0.8 4 .8 MAX. +0.10 2 ö 0.5 ±0.2 NIN □ 0.10 1.27 0.78 MAX. 0.05 0.40 ^{+0.10} \oplus 0.12 M

PACKAGE DRAWING (Unit: mm)

ABSOLUTE MAXIMUM RATINGS (TA = 25°C. All terminals are connected.)

PARAMETER	SYMBOL	N-CHANNEL	P-CHANNEL	UNIT	
Drain to Source Voltage (V _{GS} = 0 V)	VDSS	30 –30		V	
Gate to Source Voltage (V _{DS} = 0 V)	Vgss	±20 ∓20		V	
Drain Current (DC)	ID(DC)	±10	∓10	А	
Drain Current (pulse) Note1	ID(pulse)	±40	∓40	А	
Total Power Dissipation (1 unit) Note2	Ρτ1	1.7		W	
Total Power Dissipation (2 units) Note2	P _{T2}	2.0		W	
Channel Temperature	Tch	150		°C	
Storage Temperature	Tstg	-55 to +150		°C	
Single Avalanche Current Note3	ngle Avalanche Current Note3 IAS 10		-10	А	
Single Avalanche Energy Note3	Eas	10		mJ	

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Mounted on ceramic substrate of 2000 mm² x 1.6 mm

3. Starting T_{ch} = 25°C, V_{DD} = 15 V, R_G = 25 Ω , L = 100 μ H, V_{GS} = 20 \rightarrow 0 V

ELECTRICAL CHARACTERISTICS (TA = 25°C. All terminals are connected.)

N-channel

NEC

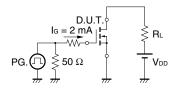
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	loss	V _{DS} = 30 V, V _{GS} = 0 V			10	μA
Gate Leakage Current	lgss	V _{GS} = ±20 V, V _{DS} = 0 V			±10	μA
Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	1.5	2.0	2.5	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 5 A	5	10		S
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = 10 V, I _D = 5 A		10	12.5	mΩ
	RDS(on)2	V _{GS} = 4.5 V, I _D = 5 A		14.5	21	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V,		2200		pF
Output Capacitance	Coss	V _{GS} = 0 V,		380		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		250		pF
Turn-on Delay Time	td(on)	V _{DD} = 15 V, I _D = 5 A,		9.6		ns
Rise Time	tr	V _{GS} = 10 V,		21		ns
Turn-off Delay Time	td(off)	R _G = 0 Ω		52		ns
Fall Time	tr			12		ns
Total Gate Charge	QG	I _D = 10 A,		42		nC
Gate to Source Charge	Q _{GS}	V _{DD} = 24 V,		6.2		nC
Gate to Drain Charge	Qgd	V _{GS} = 10 V		13		nC
Body Diode Forward Voltage ^{Note}	VF(S-D)	IF = 10 A, VGS = 0 V		0.83	1.5	V
Reverse Recovery Time	trr	IF = 10 A, VGS = 0 V,		30		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		22		nC

Note Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY

D.U.T ấг Rg = 25 Ω ~~~~ \leq 50 Ω PG. L) V_{DD} $V_{\text{GS}} = 20 \rightarrow 0 \ V$ BVDSS las VDD -Starting Tch

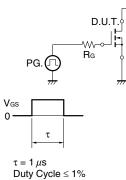
TEST CIRCUIT 3 GATE CHARGE

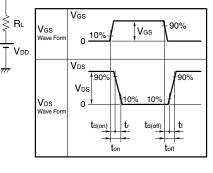


TEST CIRCUIT 2 SWITCHING TIME

Т

 $\overline{}$





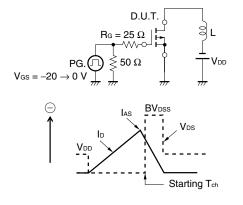
P-channel

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = -30 V, V _{GS} = 0 V			-10	μA
Gate Leakage Current	lgss	V _{GS} = ∓20 V, V _{DS} = 0 V			∓10	μA
Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = -10 V, I _D = -1 mA	-1.0	-1.7	-2.5	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = -10 V, I _D = -5 A	6	12.9		S
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = -10 V, I _D = -5 A		14	18	mΩ
	RDS(on)2	V_{GS} = -4.5 V, I _D = -5 A		17.5	26	mΩ
Input Capacitance	Ciss	V _{DS} = -10 V,		2200		pF
Output Capacitance	Coss	V _{GS} = 0 V,		510		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		410		pF
Turn-on Delay Time	td(on)	$V_{DD} = -15 V$, $I_D = -5 A$,		12		ns
Rise Time	tr	V _{GS} = -10 V,		19		ns
Turn-off Delay Time	td(off)	R _G = 0 Ω		130		ns
Fall Time	tr			36		ns
Total Gate Charge	QG	I _D = -10 A,		47		nC
Gate to Source Charge	QGS	$V_{DD} = -24 V,$		5.2		nC
Gate to Drain Charge	Qgd	V _{GS} = -10 V		15		nC
Body Diode Forward Voltage Note	VF(S-D)	IF = 10 A, V _{GS} = 0 V		0.87	1.5	V
Reverse Recovery Time	trr	$I_F = -10 \text{ A}, \text{ V}_{GS} = 0 \text{ V},$		57		ns
Reverse Recovery Charge	Qrr	di/dt = –50 A/µs		41		nC

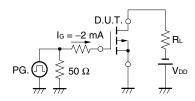
Note Pulsed

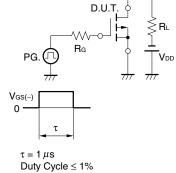
TEST CIRCUIT 1 AVALANCHE CAPABILITY

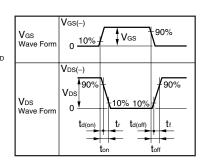
TEST CIRCUIT 2 SWITCHING TIME



TEST CIRCUIT 3 GATE CHARGE

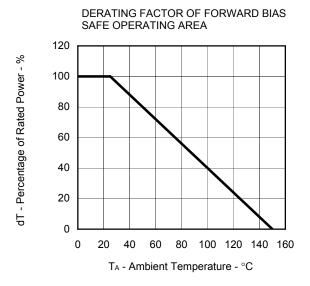




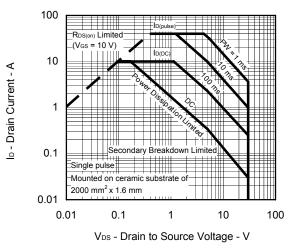


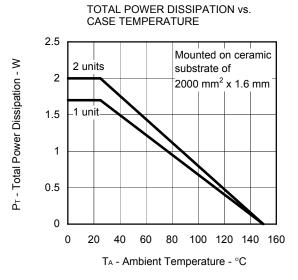
TYPICAL CHARACTERISTICS (TA = 25°C)

(1) N-channel

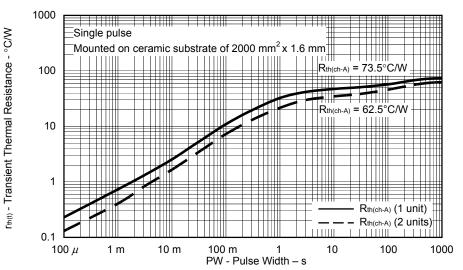


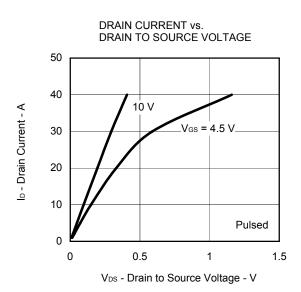




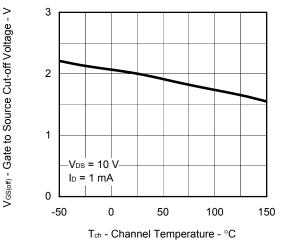


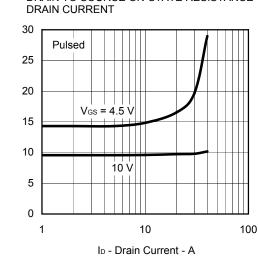




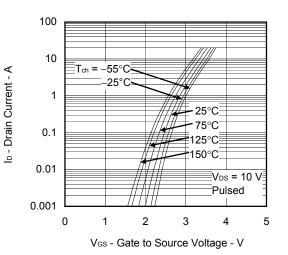




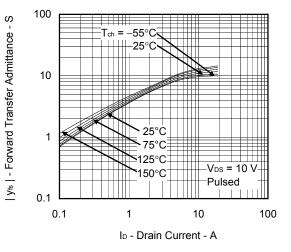




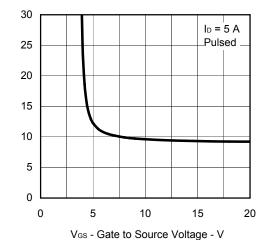
FORWARD TRANSFER CHARACTERISTICS



FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



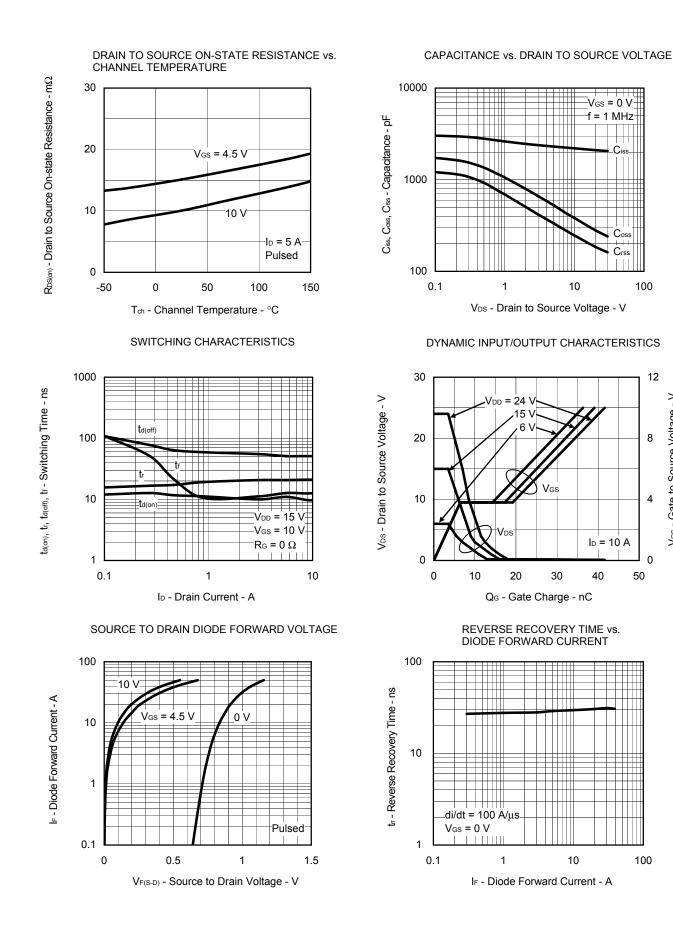
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN DRAIN CURRENT GATE

 $R_{DS(cn)}$ - Drain to Source On-state Resistance - m Ω

 $R_{DS(cn)}$ - Drain to Source On-state Resistance - m Ω

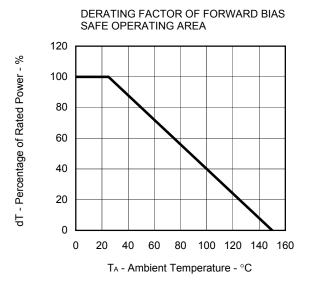


Data Sheet G19920EJ1V0DS

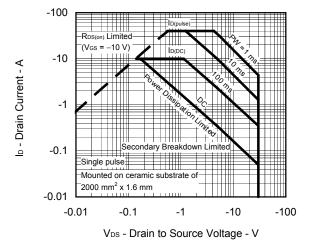
7

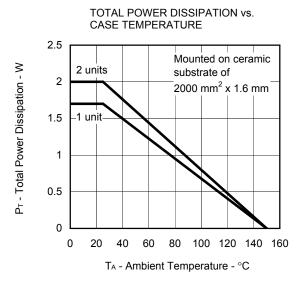
V_{GS} - Gate to Source Voltage - V

(2) P-channel

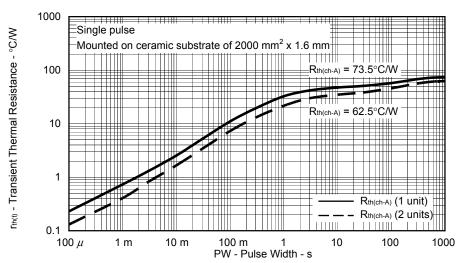


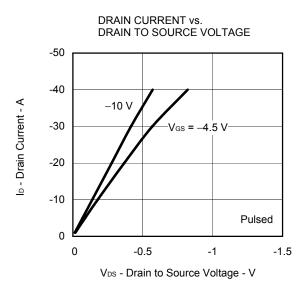
FORWARD BIAS SAFE OPERATING AREA



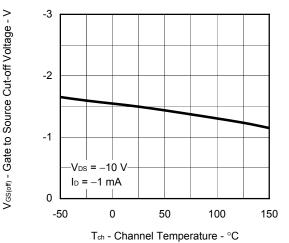


TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

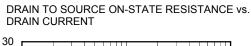


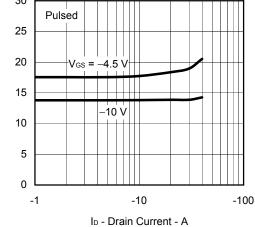




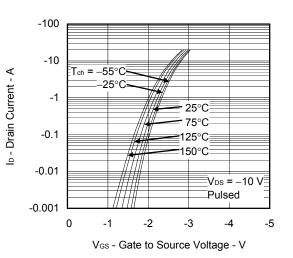




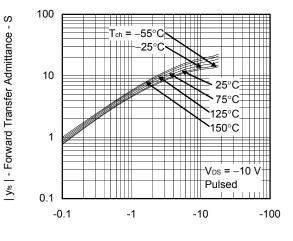




FORWARD TRANSFER CHARACTERISTICS

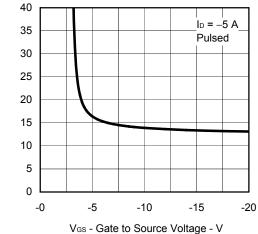


FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

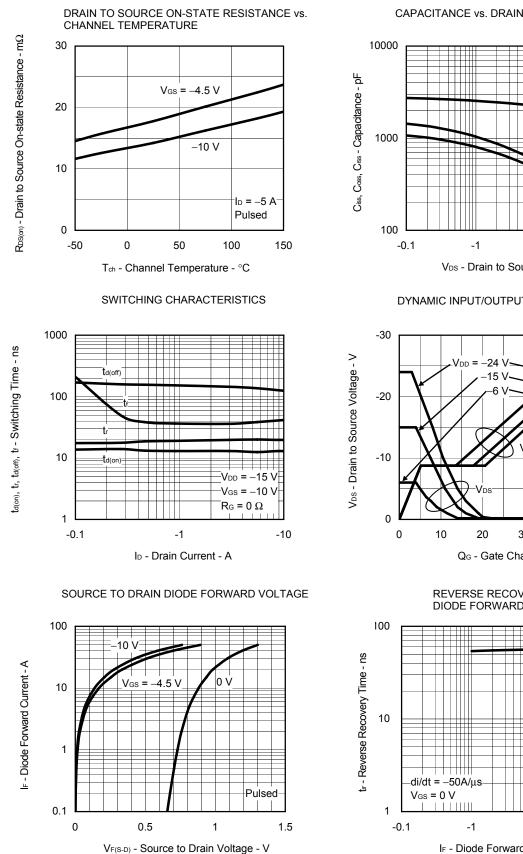




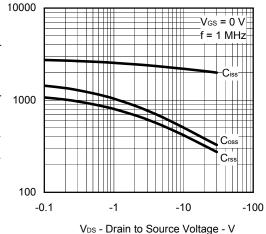
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



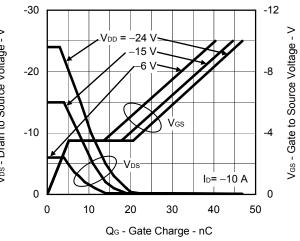
 $R_{DS(cn)}$ - Drain to Source On-state Resistance - $m\Omega$

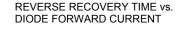


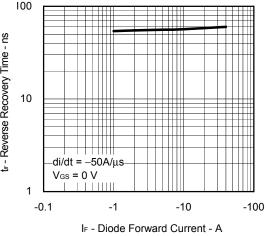
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



DYNAMIC INPUT/OUTPUT CHARACTERISTICS



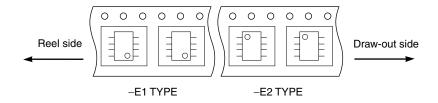




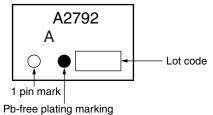
TAPE INFORMATION

NEC

There are two types (-E1, -E2) of taping depending on the direction of the device.



MARKING INFORMATION



RECOMMENDED SOLDERING CONDITIONS

The μ PA2792AGR should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, please contact an NEC Electronics sales representative.

For technical information, see the following website.

Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

Soldering Method	Soldering Conditions	Recommended Condition Symbol
Infrared reflow	Maximum temperature (Package's surface temperature): 260°C or below	IR60-00-3
	Time at maximum temperature: 10 seconds or less	
	Time of temperature higher than 220°C: 60 seconds or less	
	Preheating time at 160 to 180°C: 60 to 120 seconds	
	Maximum number of reflow processes: 3 times	
	Maximum chlorine content of rosin flux (percentage mass): 0.2% or less	
Partial heating	Maximum temperature (Pin temperature): 350°C or below	P350
	Time (per side of the device): 3 seconds or less	
	Maximum chlorine content of rosin flux: 0.2% (wt.) or less	

Caution Do not use different soldering methods together (except for partial heating).

- The information in this document is current as of August, 2009. The information is subject to change without notice. For actual design-in, refer to the latest publications of NEC Electronics data sheets, etc., for the most up-to-date specifications of NEC Electronics products. Not all products and/or types are available in every country. Please check with an NEC Electronics sales representative for availability and additional information.
- No part of this document may be copied or reproduced in any form or by any means without the prior written consent of NEC Electronics. NEC Electronics assumes no responsibility for any errors that may appear in this document.
- NEC Electronics does not assume any liability for infringement of patents, copyrights or other intellectual
 property rights of third parties by or arising from the use of NEC Electronics products listed in this document
 or any other liability arising from the use of such products. No license, express, implied or otherwise, is
 granted under any patents, copyrights or other intellectual property rights of NEC Electronics or others.
- Descriptions of circuits, software and other related information in this document are provided for illustrative
 purposes in semiconductor product operation and application examples. The incorporation of these
 circuits, software and information in the design of a customer's equipment shall be done under the full
 responsibility of the customer. NEC Electronics assumes no responsibility for any losses incurred by
 customers or third parties arising from the use of these circuits, software and information.
- While NEC Electronics endeavors to enhance the quality and safety of NEC Electronics products, customers
 agree and acknowledge that the possibility of defects thereof cannot be eliminated entirely. In addition, NEC
 Electronics products are not taken measures to prevent radioactive rays in the product design. When customers
 use NEC Electronics products with their products, customers shall, on their own responsibility, incorporate
 sufficient safety measures such as redundancy, fire-containment and anti-failure features to their products in
 order to avoid risks of the damages to property (including public or social property) or injury (including death) to
 persons, as the result of defects of NEC Electronics products.
- NEC Electronics products are classified into the following three quality grades: "Standard", "Special" and "Specific".

The "Specific" quality grade applies only to NEC Electronics products developed based on a customerdesignated "quality assurance program" for a specific application. The recommended applications of an NEC Electronics product depend on its quality grade, as indicated below. Customers must check the quality grade of each NEC Electronics product before using it in a particular application.

- "Standard": Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots.
- "Special": Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support).
- "Specific": Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems and medical equipment for life support, etc.

The quality grade of NEC Electronics products is "Standard" unless otherwise expressly specified in NEC Electronics data sheets or data books, etc. If customers wish to use NEC Electronics products in applications not intended by NEC Electronics, they must contact an NEC Electronics sales representative in advance to determine NEC Electronics' willingness to support a given application.

(Note)

- "NEC Electronics" as used in this statement means NEC Electronics Corporation and also includes its majority-owned subsidiaries.
- (2) "NEC Electronics products" means any product developed or manufactured by or for NEC Electronics (as defined above).