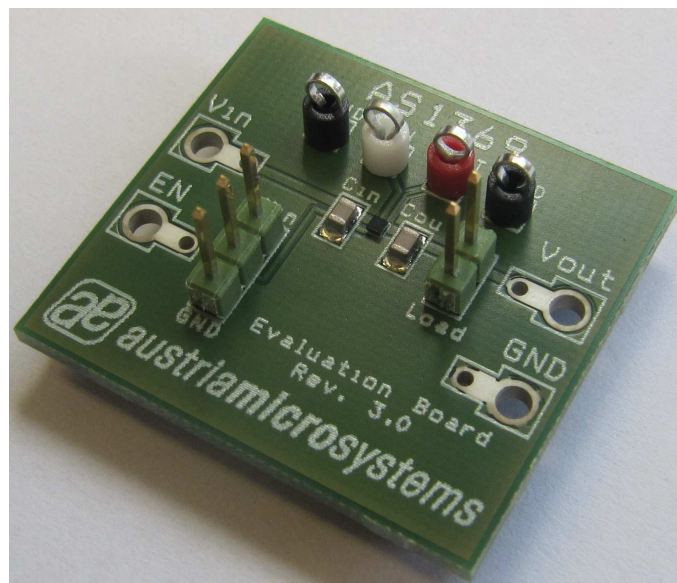**Demo Board Manual**

AS1369

200mA, Ultra-Compact Low Dropout Regulator

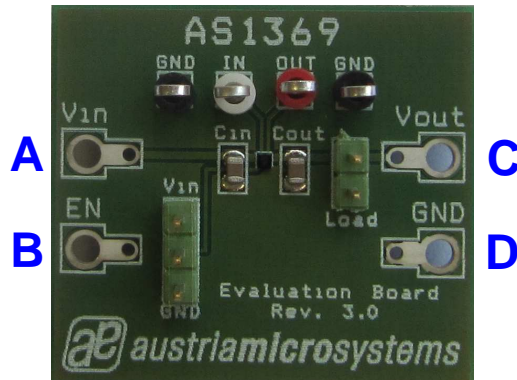www.austriamicrosystems.com/LDO/AS1369

General Description

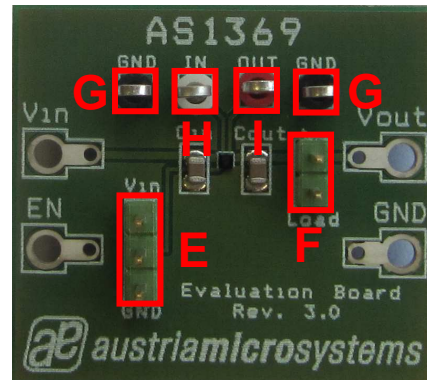## Board Description

Figure 1: Board Description - Connectors

Figure 2: Board Description – Measurement Points

Connector Description

Label	Name	Description	Info
A	**Vin**	Supply Voltage	Supply voltage ranging from 2.0V to 5.5V
B	**EN**	Active High Enable Input	$V_{EN} \geq 1.2V$: Vout is enabled $V_{EN} \leq 0.4V$: Vout is disabled This pin must not float!
C	**Vout**	Output Voltage	Output voltage ranging from 1.2V to 5.0V
D	**GND**	Ground	

Measurement Point Description

Label	Name	Description	Info
E	**EN**	Enable	ON: The AS1369 is enabled. OFF; The AS1369 is disabled. No Jumper: Connect a valid enable signal via external connector "B".
F	**Load**	Load	Jumper for resistive load
G	**GND**	Ground	Measurement Points
H	**IN**	Input Voltage	
I	**OUT**	Output Voltage	

Bill of Materials

Ref.	Function	Value	Description	Manufacturer	Mfg. Order Nr.
C1	Input Capacitor	10µF	0805 / X5R / 16V	Murata	GRM21BR71H474KA88
C2	Output Capacitor	4.7µF	0805 / X5R / 16V	Murata	GRM21BR71H474KA88
U1	LDO	marking: ASxx	8-pin WL-CSP 1.515x1.625x0.6mm	Austriamicrosystems AG	AS1369-BWLT-xx

Layout of Demo Board
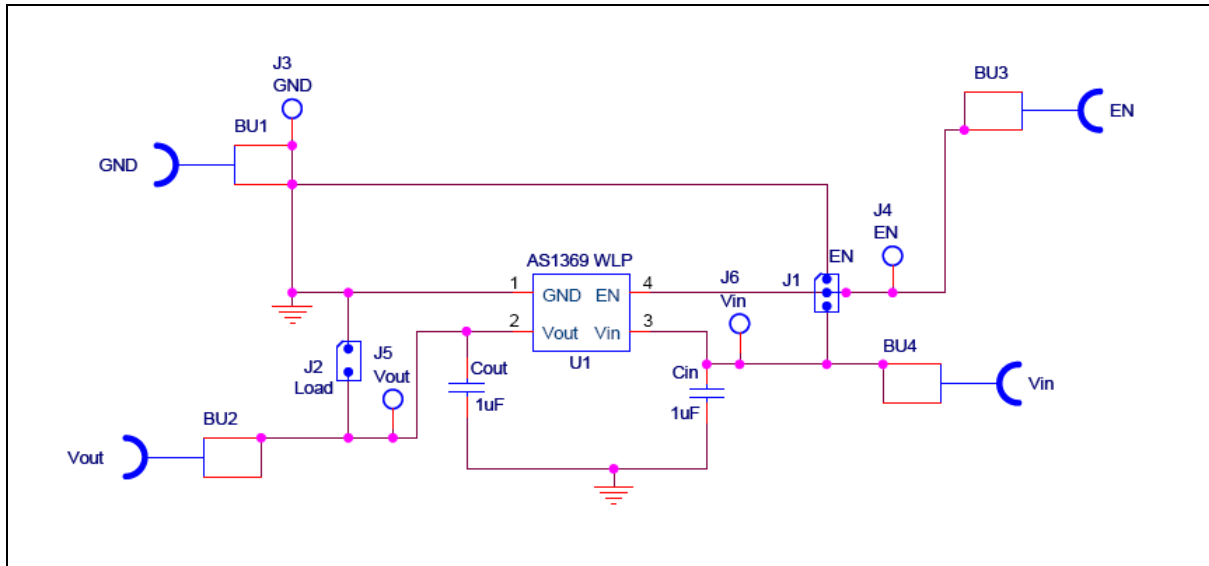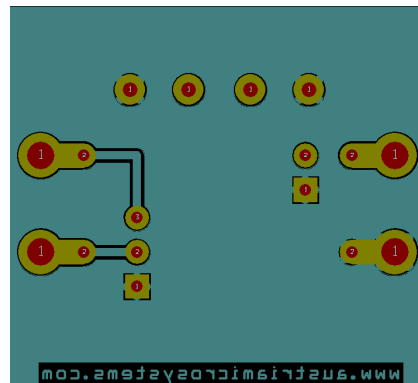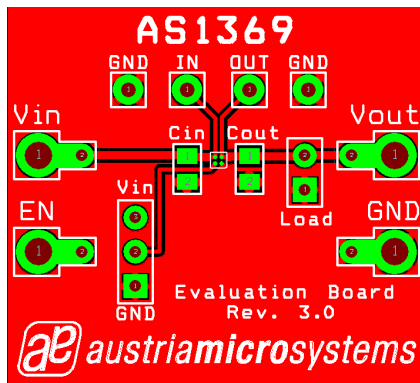
Board schematics and layout

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Figure 3: Schematics

Figure 4: Top + Bottom Layer

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