FEATURES

- DVB-ASI auto-configuration
- DVB-ASI sync word detection and 8b/10b decoding
- built-in re-clocker
- reclocked serial digital output buffer / cable driver
- 27MHz parallel digital output
- 1.8V core power supply and 3.3V charge pump power supply
- 3.3V digital I/O supply
- JTAG test interface
- small footprint compatible with GS7062
- low power operation (estimate of 375mW)

APPLICATIONS

- DVB-ASI Serial Digital Interfaces

## AVAILABLE PACKAGING

56-pin QFN

DESCRIPTION

The GS7060 is a DVB-ASI deserializer. When used in conjuction with the GO1525 voltage controlled oscillator and the GS9064 cable equalizer a DVB-ASI receive solution can be realized.

The GS7060 provides internal 8b/10b decoding and K28.5 sync word detection

The integrated reclocker features a very wide Input Jitter Tolerance (IJT) of ±0.3 UI, a rapid (typically less than 265µs) asynchronous lock time, and full compliance with DVB-ASI data streams.

A cable driver output is also provided for serial input loop through applications. The serial digital output can be selected as either before or after the integrated reclocker. The cable driver also features an output mute on loss of signal.

The GS7062 has a small 56-pin QFN package that is compatible with the GS7062. It consumes less than 375mW (preliminary estimate).
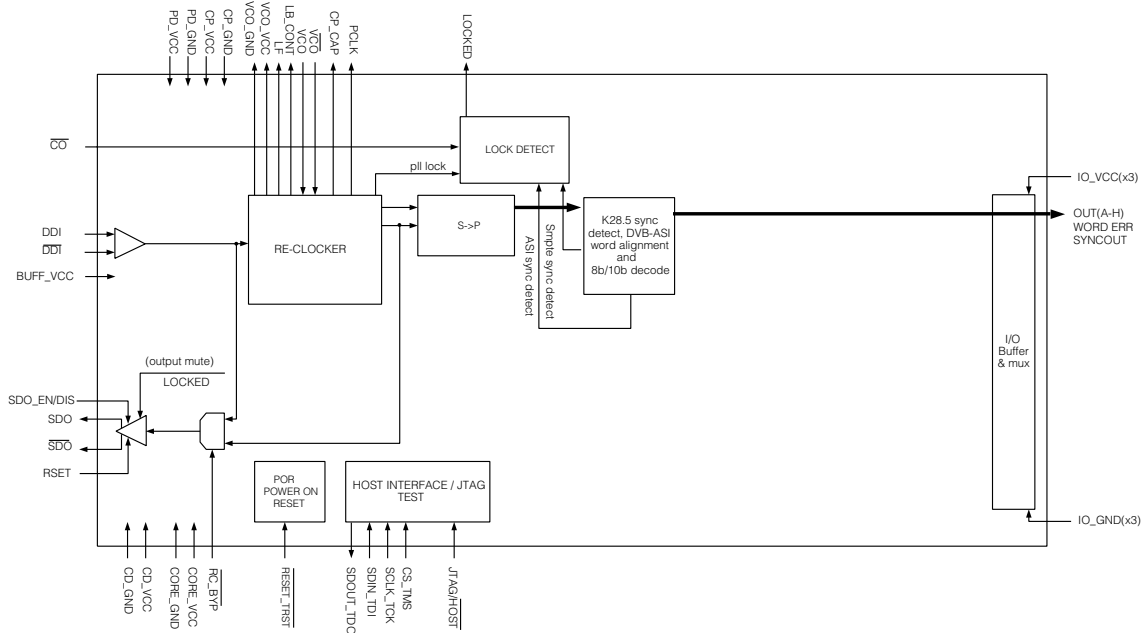
GS7060

GS7060 FUNCTIONAL BLOCK DIAGRAM

DOCUMENT IDENTIFICATION
PRODUCT BRIEF

REVISION NOTES:
New Document.

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