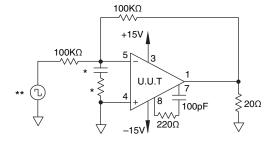


Table 4 Group A Inspection

SG	PARAMETER	SYMBOL	TEMP.	POWER	TEST CONDITIONS	MIN	MAX	UNITS
1 1 1 1 1 1 1	Quiescent Current Input Offset Voltage Input Offset Voltage Input Offset Voltage Input Bias Current, +IN Inout Bias Current, -IN Input Offset Current	I	25°C 25°C 25°C 25°C 25°C 25°C 25°C	±35V ±35V ±12V ±40V ±35V ±35V	$\begin{aligned} &V_{IN} = 0, A_{V} = 100 \\ &V_{IN} = 0 \\ &V_{IN} = 0 \\ &V_{IN} = 0 \end{aligned}$		85 3 5.3 3.5 100 100 50	mA mV mV pA pA pA
3 3 3 3 3 3	Quiescent Current Input Offset Voltage Input Offset Voltage Input Offset Voltage Input Bias Current, +IN Input BiasCurrent, -IN Input Offset Current	I _Q	-55°C -55°C -55°C -55°C -55°C -55°C	±35V ±35V ±12V ±40V ±35V ±35V	$\begin{aligned} &V_{IN} = 0, A_{V} = 100 \\ &V_{IN} = 0 \\ &V_{IN} = 0 \\ &V_{IN} = 0 \end{aligned}$		165 5.4 7.7 5.9 100 100 50	mA mV mV pA pA pA
2 2 2 2 2 2 2	Quiescent Current Input Offset Voltage Input Offset Voltage Input Offset Voltage Input Bias Current, +IN Input Bias Current, -IN Input Offset Current	I	125°C 125°C 125°C 125°C 125°C 125°C 125°C	±35V ±35V ±12V ±40V ±35V ±35V	$\begin{aligned} &V_{_{IN}}=0,A_{_{V}}=100\\ &V_{_{IN}}=0,A_{_{V}}=100\\ &V_{_{IN}}=0,A_{_{V}}=100\\ &V_{_{IN}}=0,A_{_{V}}=100\\ &V_{_{IN}}=0\\ &V_{_{IN}}=0\\ &V_{_{IN}}=0\\ &V_{_{IN}}=0 \end{aligned}$		140 6 8.3 6.5 10 10	mA mV mV mV nA nA
4 4 4 4 4 4	Output Voltage, I _o = 3A Output Voltage, I _o = 66mA Output Voltage, I _o = 2A Current Limits Stability/Noise Slew Rate Open Loop Gain Common Mode Rejection	V° V° I° EN SR A° CMR	25°C 25°C 25°C 25°C 25°C 25°C 25°C 25°C	±21.3V ±40V ± 38V ±32.2V ±35V ±35V ±35V ±35V	$\begin{aligned} & R_{_L} = 3.75\Omega \\ & R_{_L} = 500\Omega \\ & R_{_L} = 15\Omega \\ & R_{_L} = 3.75\Omega \\ & R_{_L} = 500\Omega, A_{_V} = 1, C_{_L} = 1.5nF \\ & R_{_L} = 500\Omega \\ & R_{_L} = 500\Omega, F = 10Hz \\ & R_{_L} = 500\Omega, F = DC, V_{_{CM}} = \pm 22.5V \end{aligned}$	11.3 33 30 3.4 25 80 64	6 1 500	V V V A mV V/µs dB dB
6 6 6 6 6	Output Voltage, $I_0 = 3A$ Output Voltage, $I_0 = 66mA$ Output Voltage, $I_0 = 2A$ Stability/Noise Slew Rate Open Loop Gain Common Mode Rejection	V° V° V° E _N SR A° CMR	-55°C -55°C -55°C -55°C -55°C -55°C	±21.3V ±40V ±38V ±35V ±35V ±35V ±34.5V	$\begin{aligned} & R_{_L} = 3.75\Omega \\ & R_{_L} = 500\Omega \\ & R_{_L} = 15\Omega \\ & R_{_L} = 500\Omega, A_{_V} = 1, C_{_L} = 1.5nF \\ & R_{_L} = 500\Omega, F = 10Hz \\ & R_{_L} = 500\Omega, F = DC, V_{_{CM}} = \pm 22.5V \end{aligned}$	11.3 33 30 25 80 64	1 500	V V V mV V/µs dB dB
5 5 5 5 5	Output Voltage, I _o = 66mA Output Voltage, I _o = 1A Stability/Noise Slew Rate Open Loop Gain Common Mode Rejection	V° V° EN SR AOL CMR	125°C 125°C 125°C 125°C 125°C 125°C	±40V ±23.5V ±35V ±35V ±35V ±34.5V	$\begin{aligned} & R_{L} = 500\Omega \\ & R_{L} = 15\Omega \\ & R_{L} = 500\Omega, A_{V} = 1, C_{L} = 1.5 n F \\ & R_{L} = 500\Omega, F_{L} = 10 Hz \\ & R_{L} = 10 Hz \\ $	33 15 20 80 64	1 500	V V mV V/µs dB dB

BURN IN CIRCUIT



- These components are used to stabilize device due to poor high frequency characteristics of burn in board.
- Input signals are calculated to result in internal power dissipation of approximately 2.1W at case temperature = 125°C.



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