

CAR2548FP series rectifier

Input: 90Vac to 264Vac; Output: 48Vdc @ 2500W; 3.3Vdc or 5 Vdc @ 1A



Applications

- 48Vdc distributed power architectures
- Cellular Base Stations
- Blade Servers
- Network Equipment
- Network Attached Storage
- Telecom Access Nodes
- Routers/Switches
- Broadband Switches
- ATE Equipment

Description

The CAR2548FP series of Front-End rectifiers provide highly efficient isolated power from worldwide input mains in a compact 1U industry standard form factor in an unprecedented power density of 27W/in³. These rectifiers complement the CAR2548DC converter, providing comprehensive solutions for systems connected either to commercial ac mains, 48/60Vdc power plants or telecom central offices. This plug and play approach offers significant advantages since systems can be reconfigured and repositioned readily by simply replacing the power supply. The high-density, front-to-back airflow is designed for minimal space utilization and is highly expandable for future growth. The industry standard PMBus compliant I²C communications buss offers a full range of control and monitoring capabilities. The SMBusAlert signal pin alerts customers automatically of any state change within the power supply.

Features

- Universal input with PFC
- Constant power characteristic
- 3 front panel LEDs: 1-input;2-output; 3 - fault
- Remote ON/OFF control of the 48Vdc output
- Remote sense on the 48Vdc output
- No minimum load requirements
- Redundant parallel operation
- Active load sharing (single wire)
- Hot Plug-ability
- Efficiency: typically 92% @ 50% load
- Standby orderable either as 3.3Vdc or 5Vdc
- Auto recoverable OC & OT protection
- Operating temperature: -10 - 70°C (de-rated above 50°C)
- Digital status & control: I²C and PMBus serial bus
- EN/IEC/UL60950-1 2nd edition; UL, CSA and VDE
- EMI: class B FCC docket 20780 part 15, EN55022
- Meets EN6100 immunity and transient standards
- Shock & vibration: NEBS GR-63-CORE, level 3

Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only, functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect the device reliability.

Parameter	Device	Symbol	Min	Max	Unit
Input Voltage: Continuous	All	V _{IN}	0	264	V _{ac}
Operating Ambient Temperature	All	TA	-10	70 ¹	°C
Storage Temperature	All	Tstg	-40	85	°C
I/O Isolation voltage to Frame (100% factory Hi-Pot tested)	All			1500	V _{ac}

Electrical Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, load, and temperature conditions.

INPUT						
Parameter	Device	Symbol	Min	Typ	Max	Unit
Operational Range	All	V _{IN}	90	110/230	264	V _{ac}
Frequency Range	All	F _{IN}	47	50/60	63	Hz
Main Output Turn_OFF	All	V _{IN}			80	V _{ac}
Maximum Input Current (V _{IN} = 180Vac, V _O = V _{O,set} , I _O =I _{O,max})	All	I _{IN}			16	A _{ac}
Cold Start Inrush Current (Excluding x-caps, 25°C, <10ms, per ETSI 300-132)	All	I _{IN}			50	A _{peak}
Efficiency (T _{amb} =25°C, V _{in} = 230Vac, V _{out} = 48Vdc, I _O =I _{O,max})	All	η		92		%
Power Factor (V _{in} =230Vac, I _O =I _{O,max})	All	PF		0.99		
Holdup time (V _{in} = 230Vac, V _{out} = 48Vdc, T _{amb} 25°C, I _O =I _{O,max})	All	T		16.8		ms
Early warning prior to loss of DC output below regulation	All			2		ms
Ride through	All	T		8.3		ms
Leakage Current (V _{in} = 250Vac, F _{in} = 60Hz)	All	I _{IN}		3		mArms
Isolation	Input/Output		3000			V _{ac}
	Input/Frame	All	1500			V _{ac}
	Output/Frame		100			V _{dc}

48V _{dc} MAIN OUTPUT								
Parameter	Device	Symbol	Min	Typ	Max	Unit		
Output Power	All	W	High Line Operation	180 – 264 Vac	0	-	2500	W
			Low Line Operation	99Vac [Z03A/Z01A]	0	-	1300/1000	W
				90 – 98 Vac [Z03A/Z01A]	0	-	1200/1000	W
Set point	All	V _{out}	47.95	48.00	48.05	V _{dc}		
Overall regulation (load, temperature, aging)	All		-3		+3	%		
Ripple and noise ²	All				540	mV _{p-p}		
Turn-ON overshoot	All				+3	%		

¹ Derated above 50°C at 2.5%/°C

² Measured across a 10µf electrolytic and a 0.1µf ceramic capacitors in parallel. 20MHz bandwidth

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48V _{dc} MAIN OUTPUT (continued)						
Parameter	Device	Symbol	Min	Typ	Max	Unit
Turn-ON delay	All	T			2	sec
Remote ON/OFF delay time	All				40	ms
Turn-ON rise time (10 – 90% of V _{out})	All				50	ms
Transient response 50% step [10%-60%, 50% - 100%] (di/dt – 1A/μs, recovery 300μs)	All	V _{out}	-5		+5	%V _{out}
Programmable range (hardware & software)	All		43.2		52.8	V _{dc}
Overvoltage protection, latched (recovery by cycling OFF/ON via hardware or software)	All	I _{out} I _{out}	58	59	60	V _{dc}
Output current	All		0		52	A _{dc}
Current limit, Hiccup (programmable level)	All		57.3		67.7	A _{dc}
Active current share	All		-5		+5	% of FL

AUXILIARY OUTPUT						
Parameter	Device	Symbol	Min	Typ	Max	Unit
Set point	All	V _{out}		3.3 / 5.0		V _{dc}
Overall regulation (load, temperature, aging)	All	V _{out}	-5		+5	%
Ripple and noise	All				50	mVp-p
Output current	All	I _{out}	0		1	A _{dc}
Overload protection -						
Overvoltage protection						
Isolation Output/Frame	All		100			V _{dc}

Environmental, Reliability					
Parameter	Min	Typ	Max	Units	Notes
Ambient Temperature					
Operating	-10 ³		50	°C	Air inlet from sea level to 5,000 feet. 7400 ft 51°C to 70°C
Altitude Operating			2250	m	
Power Derating			2.5	%/°C	
Storage	-40		85	°C	30,000 ft
Altitude non-operating			8200	m	
Over Temperature Protection		110 / 95		°C	Shutdown / Restart
Humidity					
Operating	30		95	%	Relative humidity, non-condensing
Storage	10		95	%	
Shock and Vibration acceleration			6	Grms	NEBS GR-63-CORE, Level 3, 20 -2000Hz, minimum 30 minutes
Earthquake Rating	4			Zone	NEBS GR-63-CORE, all floors, Seismic Zone 4 Designed and tested to meet NEBS specifications.
MTBF		100,000 200,000		Hrs	Full load, 50°C ambient, per Bellcore RPP Demonstrated

³ Designed to start at an ambient down to -40°C; meet spec after ≈ 30 min warm up period, may not meet operational limits below -10°C.

EMC				
Parameter	Criteria	Standard	Level	Test
AC input	Conducted emissions	EN55022, FCC Docket 20780 part 15, subpart J	A	0.15 – 30MHz
	Radiated emissions**	EN55022	A	30 – 10000MHz
	Voltage dips	EN61000-4-11	A	-30%, 10ms
			B	-60%, 100ms
			B	-100%, 5sec
	Voltage surge	EN61000-4-5	A	4kV, 1.2/50µs, common mode
A			2kV, 1.2/50µs, differential mode	
immunity	Fast transients	EN61000-4-4	B	5/50ns, 2kV (common mode)
Enclosure immunity	Conducted RF fields	EN61000-4-6	A	130dBµV, 0.15-80MHz, 80% AM
	Radiated RF fields	EN61000-4-3	A	10V/m, 80-1000MHz, 80% AM
		ENV 50140	A	
	ESD	EN61000-4-2	B	4kV contact, 8kV air

** Radiated emissions compliance is contingent upon the final system configuration.

Status and Control

Some functions have two means of monitor/control; A signal level that represents the analog value being measured or controlled, or, reading/writing via the I²C port the measured value or the control command.

Unless otherwise noted, control via the signals pins is 'active' so long that a firmware based command is not initiated. Once firmware initiates a command that is also represented on a signal pin, the firmware takes over and replaces the hardware based control signal. Firmware control is maintained until bias power to the processor is interrupted. Once bias power is removed the processor resets and the analog signal pin control is 'active' until firmware takes over control.

Details of analog controls are provided in this data sheet under Signal Definitions. GE Energy will provide separate application notes on the I2C protocol. Contact your local GE Energy representative for details.

Signal Definitions

All signals and outputs are referenced to Output return. These include 'Vstb return' and 'Signal return'.

Input Signals

Voltage programming (V_{prog}): An analog voltage on this signal can vary the output voltage ± 10% from 43.2Vdc to 52.8Vdc. The equation of this signal is:

$$V_{out} = 43.2 + 3.3 (V_{prog} - 0.09) \quad 0.09 < V_{prog} < 3$$

If V_{prog} is > 3V or left open the programming signal is ignored and the unit output is set at the setpoint of 48Vdc.

Load share (Ishare): This is a single wire analog signal that is generated and acted upon automatically by power supplies connected in parallel. The Ishare pins should be tied together for power supplies if active current share among the power supplies is desired. No resistors or capacitors should get connected to this pin.

Remote ON/OFF: Controls the presence of the main 48Vdc output voltage. This is an open collector, TTL level control signal. This signal needs to be pulled HI externally through a resistor. Maximum collector voltage is 12Vdc and the maximum sink current is 1mA. A Logic 1 (TTL HI level) turns ON the 48Vdc output, while a Logic 0 (TTL LO level) turns OFF the 48Vdc output.

A turn OFF command either through this signal (Remote ON/OFF) or firmware commanded would turn OFF the 48V output.

Enable: This is a short signal pin that controls the presence of the 48Vdc main output. This pin should be connected to 'output return' on the system side of the output connector. The purpose of this pin is to ensure that the output turns ON after engagement of the power blades and turns OFF prior to disengagement of the power blades.

Write protect (WP): This signal protects the contents of the EEPROM from accidental over writing. When left open the EEPROM is write protected. A LO (TTL compatible) permits writing to the EEPROM. This signal is pulled HI internally by the power supply.

Output signals

Output current monitor (I_{mon}): A voltage level proportional to the delivered output current is present on this pin. The signal level is 0.1V per amp ± 0.25V.

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AC OK: A TTL compatible status signal representing whether the input voltage is within the anticipated range. This signal is internally pulled HI to 3.3V via a 10kΩ resistor. Maximum sink current ≤ 20mA and the max voltage is 12Vdc. Open collector (HI) on this signal indicates that the input voltage is applied within the specified input range.

DC OK: A TTL compatible status signal representing whether the output voltage is present. This signal is internally pulled HI to 3.3V via a 10kΩ resistor. Maximum sink current ≤ 4mA and the max voltage is 12Vdc. A (HI) on this signal indicates that the output voltage is present.

Over temp warning: A TTL compatible status signal representing whether an over temperature exists. This signal needs to be pulled HI externally through a resistor. Maximum sink current ≤ 20mA and the max voltage is 12Vdc. Open collector (HI) on this signal indicates that temperatures are normal.

If an over temperature should occur, this signal would pull LO for approximately 10 seconds prior to shutting down the power supply. The unit would restart if internal temperatures recover within normal operational levels. At that time the signal reverts back to its open collector (HI) state.

Fault: A TTL compatible status signal representing whether a Fault occurred. This signal needs to be pulled HI externally through a resistor. Maximum sink current ≤ 20mA and the max voltage is 12Vdc. Open collector (HI) on this signal indicates that no Fault is present.

This signal activates for OTP, OVP, OCP, AC fault or No output.

PS Present: This pin is connected to 'output return' within the power supply. Its intent is to indicate to the system that a power supply is present. This signal may need to be pulled HI externally through a resistor.

Interrupt: A TTL compatible status signal, representing the SMBusAlert# feature of the PMBus compatible I²C protocol in the power supply. This signal needs to be pulled HI externally through a resistor. Maximum sink current ≤ 4mA and the pull up resistor should be tied to 3.3Vdc. Open collector (HI) on this signal indicates that no Interrupt has been triggered.

Serial Bus Communications

The I²C interface facilitates the monitoring and control of various operating parameters within the unit and transmits these on demand over an industry standard I²C Serial bus.

All signals are referenced to 'Signal Return'.

Device addressing: The microcontroller (MCU) and the EEPROM have the following addresses:

Device	Address	Address Bit Assignments (Most to Least Significant)							
MCU	0xBx	1	0	1	1	A2	A1	A0	R/W
EEPROM	0xAx	1	0	1	0	A2	A1	A0	R/W

Address lines (A2, A1, A0): These signal pins allow up to eight (8) modules to be addressed on a single I²C bus. The pins are pulled HI internal to the power supply. For a logic LO these pins should be connected to 'Output Return'

Serial Clock (SCL): The clock pulses on this line are generated by the host that initiates communications across the I²C Serial bus. This signal is pulled up internally to 3.3V by a 10kΩ resistor. The end user should add additional pull up resistance as necessary to ensure that rise and fall time timing and the maximum sink current is in compliance to the I²C specifications.

Serial Data (SDA): This line is a bi-directional data line. This signal is pulled up internally to 3.3V by a 10kΩ resistor. The end user should add additional pull up resistance as necessary to ensure that rise and fall time timing and the maximum sink current is in compliance to the I²C specifications.

EEPROM

The microcontroller has 96 bytes of EEPROM memory available for the system host.

Another separate EEPROM IC will provide another 128 bytes of memory with write protect feature. Minimum information to be included in this separate EEPROM: model number, revision, date code, serial number etc.

See the communications protocol for further information.

Communications Protocol

The I²C protocol is described in detail by the *I²C and PMBus Serial Communications Protocol for the CAR Family of Power Supplies* application note.

The following I²C protocol commands are not supported:

FAN1_SPEED_I²C
FAN2_SPEED_I²C

The following PMBus protocol commands are not supported:

FAN_COMMAND_1	0 x 21
STATUS_FAN_1_2	0 x 81
READ_VIN	0 x 88
READ_IIN	0 x 89
READ_FAN_SPEED_1	0 x 90
READ_FAN_SPEED_2	0 x 91
READ_PIN	0 x A3

LEDs

Three LEDs are located on the front faceplate. The AC_OK LED provides visual indication of the INPUT signal function. When the LED is ON GREEN the power supply input is within normal design limits.

When the DC_OK LED is GREEN the DC output is present.

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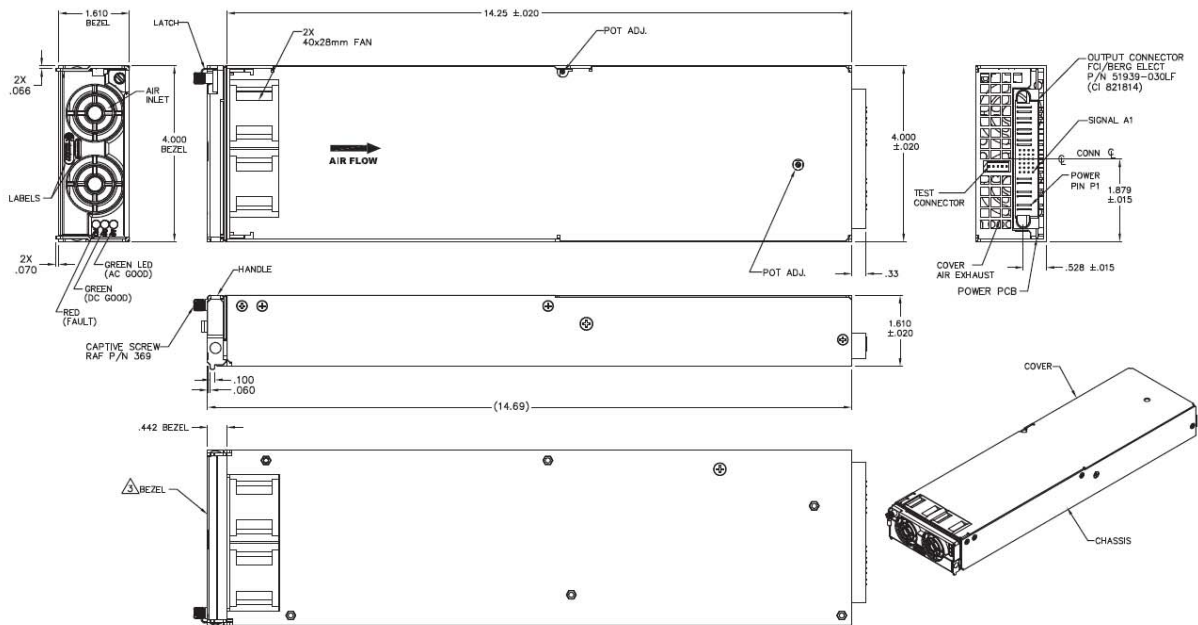
When the FAULT_LED is RED then a fault condition exists and the power supply may not provide output power. The table below further defines these states:

Alarm Table

Test Condition	LED Indicator			Monitoring Signals			
	AC OK	DC OK	FAULT	FAULT	DC OK	INPUT OK	TEMP OK
1 Normal Operation	Green	Green	OFF	High	High	High	High
2 Low or NO INPUT	OFF	OFF	Red	Low	Low	Low	High
3 OVP	Green	OFF	Red	Low	Low	High	High
4 Over Current	Green	OFF	Red	Low	Low	High	High
5 Over Temp Alarm	Green	Green	Red	Low	High	High	Low
6 Over Temp Fault	Green	OFF	Red	Low	Low	High	Low

Note: Test condition #2 has 2 modules working in parallel. One module is running and the other has no AC.

Outline Drawing



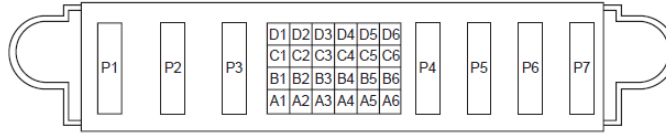
All Dimensions in Inches (mm)
Tolerance: .XX = ± .02 in (.50 mm)
.XXX = ± .010 in (.254 mm)

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Connector Pin Assignments

Mating Connector: FCI Berg P/N: 51939-030LF or Molex 87663-9002
Mating connector: 51915-051LF or Molex 87664-2004

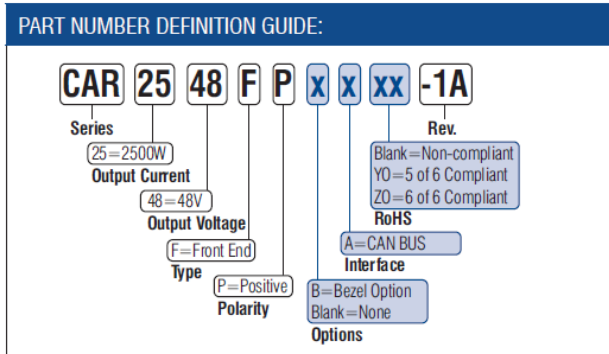


Pin	Function	Pin	Function	Pin	Function	Pin	Function
A1	Vstb [3.3V]	B1	Fault	C1	ISHARE	D1	VProg
A2	Vstb [3.3V] Return	B2	I Monitor (IMON)	C2	N/C	D2	OVP Test Point
A3	Signal Return	B3	PS Present	C3	Over Temp Warning	D3	Remote ON/OFF
A4	Write Protect (WP)	B4	Enable: "0" -ON "1" -OFF	C4	I ² C Address (A0)	D4	DC OK
A5	Remote Sense (+)	B5	SDA (I ² C bus)	C5	I ² C Address (A1)	D5	AC OK
A6	Remote Sense (-)	B6	SCL (I ² C bus)	C6	I ² C Address (A2)	D6	Interrupt
P1	Line	P2	Neutral	P3	Chassis		
P4	+Vout	P5	+Vout	P6	Output Return	P7	Output Return

Ordering Information

Please contact your GE Energy Sales Representative for pricing, availability and optional features.

PRODUCT	DESCRIPTION	PART NUMBER
2500W Front-End	+48Vout Front-End, 3.3Vaux, low line power capacity 1000W	CAR2548FP-Z01A
2500W Front-End	+48Vout Front-End, 3.3Vaux	CAR2548FP-Z03A
10Kw Shelf	Shelf for CAR2548FP - hold 5 modules	ACE254RUW-1A



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