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April 1st, 2010 Renesas Electronics Corporation

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MOS FIELD EFFECT TRANSISTOR $\mu PA2560$

Dual N-CHANNEL MOSFET FOR SWITCHING

DESCRIPTION

The μ PA2560 is Dual N-channel MOSFETs designed for Back light inverters and power management applications of portable equipments. Dual N-channel MOSFETs are assembled in one package, to contribute minimize the equipments.

FEATURES

- 4.5 V drive available
- · Low on-state resistance

 $R_{DS(on)1}$ = 50 m Ω MAX. (V_{GS} = 10 V, I_D = 2 A) $R_{DS(on)2}$ = 83 m Ω MAX. (V_{GS} = 4.5 V, I_D = 2 A)

- Built-in gate protection diode
- Small and surface mount package (8-pin VSOF (2429))

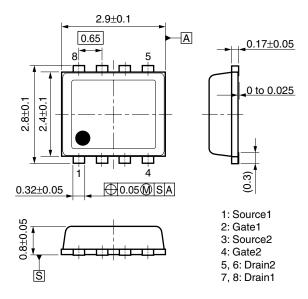
ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (V _{GS} = 0 V)	V_{DSS}	30	V
Gate to Source Voltage (V _{DS} = 0 V)	Vgss	±20	V
Drain Current (DC)	$I_{D(DC)}$	±4.5	Α
Drain Current (pulse) Note1	I _{D(pulse)}	±18	Α
Total Power Dissipation (1 unit, 5 s) Note2	P _{T1}	1.5	W
Total Power Dissipation (2 units, 5 s) Note2	P _{T2}	2.2	W
Channel Temperature	Tch	150	°C
Storage Temperature	T_{stg}	-55 to +150	°C

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Mounted on FR-4 board of 25.4 mm x 25.4 mm x 0.8 mm.

PACKAGE DRAWING (Unit: mm)



EQUIVALENT CIRCUIT (1/2)

Gate Protection Diode

ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE
μPA2560T1H-T1-AT Note		8 mm embossed taping	
μPA2560T1H-T2-AT Note	Pure Sn	3000 p/reel	8-pin VSOF (2429)

Note Pb-free (This product does not contain Pb in the external electrode and other parts.)

Marking: 2560

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

Caution This product is electrostatic-sensitive device due to low ESD capability and should be handled with caution for electrostatic discharge.

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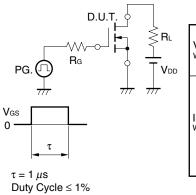


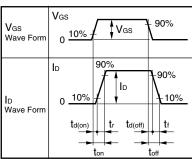
ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 30 V, V _{GS} = 0 V			1	μΑ
Gate Leakage Current	Igss	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$			±10	μΑ
Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	1.0		2.5	V
Forward Transfer Admittance Note	yfs	V _{DS} = 10 V, I _D = 2 A	1			S
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = 10 V, I _D = 2 A		38	50	mΩ
	RDS(on)2	V _{GS} = 4.5 V, I _D = 2 A		48	83	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V,		310		pF
Output Capacitance	Coss	$V_{GS} = 0 V$,		65		pF
Reverse Transfer Capacitance	Crss	f = 1.0 MHz		27		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 15 V, I _D = 2 A,		6		ns
Rise Time	tr	V _{GS} = 10 V,		2.8		ns
Turn-off Delay Time	t _{d(off)}	$R_G = 6 \Omega$		15		ns
Fall Time	tf			2.4		ns
Total Gate Charge	Q _G	V _{DD} = 24 V, V _{GS} = 10 V,				
		I _D = 4.5 A		6.6		nC
Body Diode Forward Voltage Note	V _{F(S-D)}	I _F = 4.5 A, V _{GS} = 0 V		0.9		V

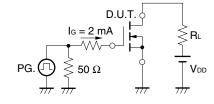
Note Pulsed

TEST CIRCUIT 1 SWITCHING TIME



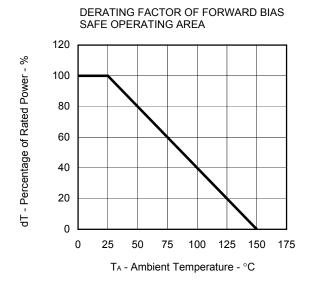


TEST CIRCUIT 2 GATE CHARGE

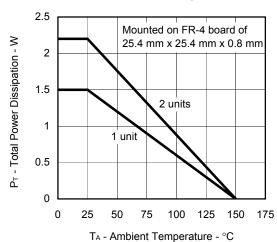




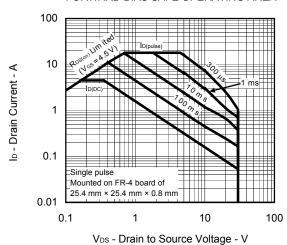
TYPICAL CHARACTERISTICS (TA = 25°C)



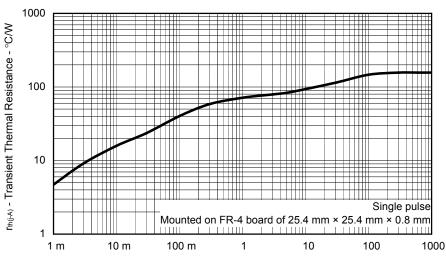
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



FORWARD BIAS SAFE OPERATING AREA



TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



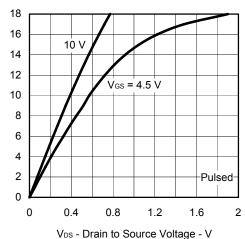
PW - Pulse Width - s

3

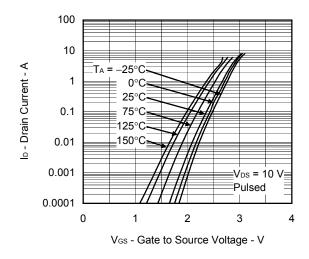


lo - Drain Current - A

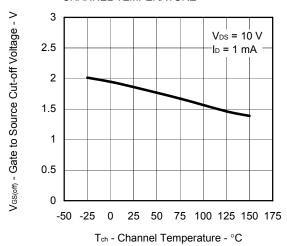
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



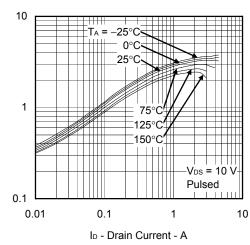
FORWARD TRANSFER CHARACTERISTICS



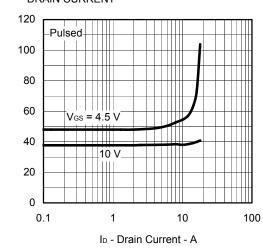
GATE TO SOURCE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



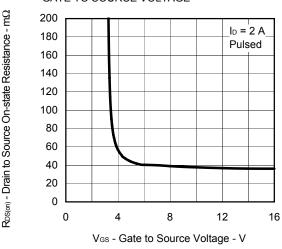
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



R_{DS(m)} - Drain to Source On-state Resistance - mΩ

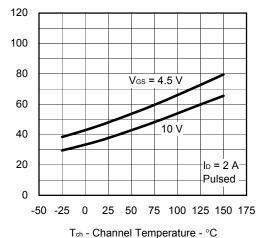
ys | - Forward Transfer Admittance -



 $\mathsf{Ros}_{(m)}$ - Drain to Source On-state Resistance - $m\Omega$

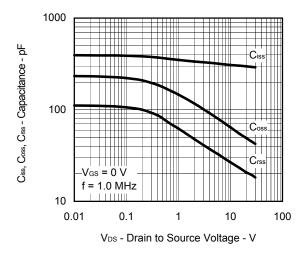
ta(on), tr, ta(off), tr - Switching Time - ns

DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE

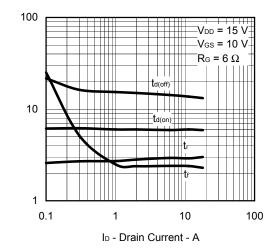


Vps - Drain to Source Voltage - V

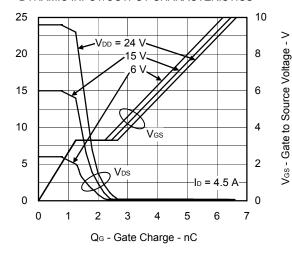
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



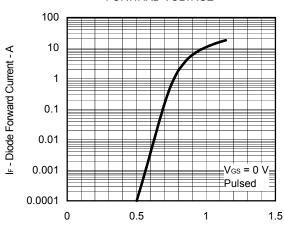
SWITCHING CHARACTERISTICS



DYNAMIC INPUT/OUTPUT CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



 $V_{F(S-D)}$ - Source to Drain Voltage - V



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