



## Maxi Family

72 V Input







## **DC-DC Converter Module**

#### **Features**

- DC input range: 43 110 V (continuous)
- Isolated output
- Encapsulated circuitry for shock and vibration resistance
- Extended temperature range (-55 to +100°C)
- Input surge withstand: 150 V for 100 ms
- DC output: 3.3 48 V
- Programmable output: 10 to 110%
- Regulation: ±0.2% no load to full load
- Efficiency: Up to 88%
- Maximum operating temp: 100 °C, full load
- Power density: up to 80 W per cubic inch
- Height above board: 0.43 in. (10,9 mm)
- Parallelable, with N+M fault tolerance
- Low noise ZCS/ZVS architecture
- RoHS Compliant (with F or G pin option)

## **Product Overview**

These DC-DC converter modules use advanced power processing, control and packaging technologies to provide the performance, flexibility, reliability and cost effectiveness of a mature power component. High frequency ZCS/ZVS switching provides high power density with low noise and high efficiency.

## **Applications**

Railway/Transportation system applications including communications systems, information display, lighting, control systems, ticket machines, passenger entertainment, public address systems, door control, industrial power systems and power generation systems.

For details on proper operation please refer to:

Design Guide & Applications Manual for Maxi, Mini, Micro Family.

## **Absolute Maximum Ratings**

Parameter	Rating	Unit	Notes
+In to -In voltage	-0.5 to +155	Vdc	
PC to –In voltage	-0.5 to +7.0	Vdc	
PR to –In voltage	-0.5 to +7.0	Vdc	
SC to –Out voltage	-0.5 to +1.5	Vdc	
-Sense to -Out voltage	1.0	Vdc	
Isolation voltage			
in to out	3000	Vrms	Test voltage
in to base	1500	Vrms	Test voltage
out to base	500	Vrms	Test voltage
Operating Temperature	-55 to +100	°C	M-Grade
Storage Temperature	-65 to +125	°C	M-Grade
Din coldering temperature	500 (260)	°F (°C)	<5 sec; wave solde
Pin soldering temperature —	750 (390)	°F (°C)	<7 sec; hand solde
Mounting torque	5 (0.57)	in-lbs (N-m)	6 each

## **Part Numbering**

e.g. V72A24C400BL



Pin Style

Blank: Short

S: Short ModuMate

N: Long ModuMate

F: Short RoHS

L: Long

Baseplate
Blank: Slotted
2: Threaded
3: Through-hole

Finish

Tin/Lead

Tin/Lead

Gold

Gold

Gold

Gold

Output	Voltage
3 V 3 =	3.3 V
<b>5</b> =	: 5 V
<b>8</b> =	: 8 V
12 =	: 12 V
15 =	: 15 V
<b>24</b> =	: 24 V
<b>28</b> =	: 28 V
<b>36</b> =	: 36 V
48 =	: 48 V

ŀ	Product Grade Temperatures (°C)									
Grade	9	Operating	Storage							
E	=	- 10 to +100	- 20 to +125							
C	=	- 20 to +100	- 40 to +125							
T	=	- 40 to +100	- 40 to +125							
Н	=	- 40 to +100	- 55 to +125							
М	=	- 55 to +100	- 65 to +125							

Outpu	l Power
Vout	Pout
3.3 V	264 W
5 V	<b>300</b> W
8 V	<b>300</b> W
12 V	<b>400</b> W
15 V	<b>400</b> W
24 V	<b>400</b> W
28 V	<b>400</b> W
36 V	<b>400</b> W
48 V	<b>400</b> W

Outnut Power

## MODULE FAMILY ELECTRICAL CHARACTERISTICS

Electrical characteristics apply over the full operating range of input voltage, output load (resistive) and baseplate temperature, unless otherwise specified. All temperatures refer to the operating temperature at the center of the baseplate.

## **■ MODULE INPUT SPECIFICATIONS**

Parameter	Min	Тур	Max	Unit	Notes
Operating input voltage	43	72	110	Vdc	Per EN50155 and GBT-25119
Input surge withstand			150	Vdc	<100 ms
Undervoltage turn-on		41.7	42.6	Vdc	
Undervoltage turn-off	35.2	36.5		Vdc	
Overvoltage turn-off/on	111	115.5	121	Vdc	
Disabled input current			1.5	mA	PC pin low

#### **■ MODULE OUTPUT SPECIFICATIONS**

Parameter	Min	Тур	Max	Unit	Notes
Output voltage setpoint			±1	% Vout nom.	Nominal input; full load; 25°C
ine regulation		±0.02	±0.20	%	Low line to high line; full load
Temperature regulation		±0.002	±0.005	%/°C	Over operating temperature range
Power sharing accuracy		±2	±5	%	10 to 100% of full load
Programming range	10		110	%	Of nominal output voltage. For trimming below 90% of nominal, a minimum load of 10% of maximum rated power may be required.
-Out to -Out, +Sense to -Out	t — Absolute Ma	ximum Rating	<u>s</u>		
3.3 V			-0.5 to 4.7	Vdc	Externally applied
5 V			-0.5 to 7.0	Vdc	Externally applied
8 V			-0.5 to 10.9	Vdc	Externally applied
12 V			-0.5 to 16.1	Vdc	Externally applied
			-0.5 to 20.0	Vdc	Externally applied
15 V			-0.5 10 20.0	v u c	Externally applied
15 V 24 V			-0.5 to 20.0 -0.5 to 31.7	Vdc	Externally applied
					7 11
24 V			-0.5 to 31.7	Vdc	Externally applied

**Note:** For important information relative to applications where the converter modules are subject to continuous dynamic loading, contact Vicor applications engineering at 800-927-9474.

#### **■ THERMAL RESISTANCE AND CAPACITY**

Parameter	Min	Тур	Max	Unit	
Baseplate to sink; flat, greased surface		0.08		°C/Watt	
Baseplate to sink; thermal pad (P/N 20263)		0.07		°C/Watt	
Baseplate to ambient		4.9		°C/Watt	
Baseplate to ambient; 1000 LFM		1.1		°C/Watt	
Thermal capacity		165		Watt-sec/°C	



## MODULE FAMILY ELECTRICAL CHARACTERISTICS (CONT.)

## ■ MODULE CONTROL SPECIFICATIONS

Parameter	Min	Тур	Max	Unit	Notes
PRIMARY SIDE (PC = Primar	y Control; PR =	Parallel)			
PC bias voltage current limit	5.50 1.5	5.75 2.1	6.00 3.0	Vdc mA	PC current = 1.0 mA PC voltage = 5.5 V During normal operation
PC module disable	2.3	2.6	2.9	Vdc	Switch must be able to sink ≥4 mA. See Fig. 2
PC module enable delay		4	7	ms	
PC module alarm			0.5	Vavg	UV, OV, OT, module fault. See Figs. 3 and 5
PC resistance	0.9	1.0	1.1	MΩ	See Fig. 3, converter off or fault mode
PR emitter amplitude	5.7	5.9	6.1	Volts	PR load >30 $\Omega$ , <30 pF
PR emitter current	150			mA	
PR receiver impedance	375	500	625	Ω	25°C
PR receiver threshold	2.4	2.5	2.6	Volts	Minimum pulse width: 20 ns
PR drive capability			12	modules	Without PR buffer amplifier
SECONDARY SIDE (SC = Se	condary Control	)			
SC bandgap voltage	1.21	1.23	1.25	Vdc	Referenced to -Sense
SC resistance	990	1000	1010	Ω	
SC capacitance		0.033		μF	
SC module alarm		0		Vdc	With open trim; referenced to –Sense. See Fig. 7

## **■ MODULE GENERAL SPECIFICATIONS**

Parameter	Min	Тур	Max	Unit	Notes
Remote sense (total drop)			0.5	Vdc	0.25 V per leg (sense leads must be connected to respective, output terminals)
Isolation test voltage (in to out)*	3000			Vrms	Complies with reinforced insulation requirements
Isolation test voltage (in to base)*	1500			Vrms	Complies with basic insulation requirements
Isolation test voltage (out to base)*	500			Vrms	Complies with operational insulation requirements
Isolation resistance		10		ΜΩ	in to out, in to baseplate, out to baseplate
Weight (E, C, T grade)	6.5 (184.3)	7.3 (207.5)	8.1 (230.7)	ounces (grams)	
Weight (H, M grade)	7.4 (209.3)	8.2 (232.5)	9.0 (255.7)	ounces (grams)	
Temperature limiting	100	115		°C	See Figs. 3 and 5. Do not operate coverter >100C.
Agency approvals	cU	Rus, cTÜVus,	CE		UL60950-1, EN60950-1, CSA60950-1, IEC60950- With appropriate fuse in series with the +Input

<sup>\*</sup> Isolation test voltage, 1 minute or less.

## Note:

Specifications are subject to change without notice.



## **■ MODULE SPECIFIC OPERATING SPECIFICATIONS**

## 3.3 Vout, 264W (e.g. V72A3V3C264BL)

Parameter	Min	Тур	Max	Unit	Notes
Efficiency	79	80.4		%	Nominal input; full load; 25°C
Ripple and noise		289	361	mV	p-p; Nominal input; full load; 20MHz bandwidth
Output OVP setpoint	4.14	4.3	4.46	Volts	25°C; recycle input voltage or PC to restart (>100 ms off)
Dissipation, standby		10.2	11.7	Watts	No load
Load regulation		±0.02	±0.2	%	No load to full load; nominal input
Output Current	0		80	Amps	
Current limit	81.6	92	108	Amps	Output voltage 95% of nominal
Short circuit current	56	92	108	Amps	Output voltage <250 mV

## 5 Vout, 300 W (e.g. V72A5C300BL)

Parameter	Min	Тур	Max	Unit	Notes
Efficiency	83.0	85.5		%	Nominal input; full load; 25°C
Ripple and noise		100	125	mV	p-p; Nominal input; full load; 20 MHz bandwidth
Output OVP setpoint	6.03	6.25	6.47	Volts	25°C; recycle input voltage or PC to restart (>100 ms off)
Dissipation, standby		10.4	11	Watts	No load
Load regulation		±0.02	±0.2	%	No load to full load; nominal input
Output Current	0		60	Amps	
Current limit	61.2	69	81	Amps	Output voltage 95% of nominal
Short circuit current	22	69	81	Amps	Output voltage <250 mV

## 8 Vout, 300W (e.g. V72A8C300BL)

Parameter	Min	Тур	Max	Unit	Notes
Efficiency	83.6	84.8		%	Nominal input; full load; 25°C
Ripple and noise		215	269	mV	p-p; Nominal input; full load; 20MHz bandwidth
Output OVP setpoint	9.36	9.7	10.1	Volts	25°C; recycle input voltage or PC to restart (>100 ms off)
Dissipation, standby		10.2	10.7	Watts	No load
Load regulation		±0.02	±0.2	%	No load to full load; nominal input
Output Current	0		37.5	Amps	
Current limit	38.2	43.1	50.7	Amps	Output voltage 95% of nominal
Short circuit current	26.2	43.1	50.7	Amps	Output voltage <250 mV

## 12 Vout, 400 W (e.g. V72A12C400BL)

Parameter	Min	Тур	Max	Unit	Notes
Efficiency	86.0	87.3		%	Nominal input; full load; 25°C
Ripple and noise		320	400	mV	p-p; Nominal input; full load; 20 MHz bandwidth
Output OVP setpoint	13.7	14.3	14.9	Volts	25°C; recycle input voltage or PC to restart (>100 ms off)
Dissipation, standby		12.4	13.5	Watts	No load
Load regulation		±0.02	±0.2	%	No load to full load; nominal input
Output Current	0		33.33	Amps	
Current limit	33.9	38.3	45	Amps	Output voltage 95% of nominal
Short circuit current	23.3	38.3	45	Amps	Output voltage <250 mV

## 15 Vout, 400 W (e.g. V72A15C400BL)

Parameter	Min	Тур	Max	Unit	Notes
Efficiency	85.2	86.7		%	Nominal input; full load; 25°C
Ripple and noise		300	375	mV	p-p; Nominal input; full load; 20 MHz bandwidth
Output OVP setpoint	17.1	17.8	18.5	Volts	25°C; recycle input voltage or PC to restart (>100 ms off)
Dissipation, standby		10.4	11.2	Watts	No load
Load regulation		±0.02	±0.2	%	No load to full load; nominal input
Output Current	0		26.67	Amps	
Current limit	27.2	30.7	36.1	Amps	Output voltage 95% of nominal
Short circuit current	18.6	30.7	36.1	Amps	Output voltage <250 mV



## **■ MODULE SPECIFIC OPERATING SPECIFICATIONS**

## 24 Vout, 400 W (e.g. V72A24C400BL)

Parameter	Min	Тур	Max	Unit	Notes
Efficiency	86.5	87.9		%	Nominal input; full load; 25°C
Ripple and noise		200	250	mV	p-p; Nominal input; full load; 20 MHz bandwidth
Output OVP setpoint	27.1	28.1	29.1	Volts	25°C; recycle input voltage or PC to restart (>100 ms off)
Dissipation, standby		11	12.5	Watts	No load
Load regulation		±0.02	±0.2	%	No load to full load; nominal input
Output Current	0		16.67	Amps	
Current limit	17	19.2	23.4	Amps	Output voltage 95% of nominal
Short circuit current	11.6	19.2	22.6	Amps	Output voltage <250 mV

## 28 Vout, 400W (e.g. V72A28C400BL)

Parameter	Min	Тур	Max	Unit	Notes
Efficiency	86.3	88.4		%	Nominal input; full load; 25°C
Ripple and noise		302	378	mV	p-p; Nominal input; full load; 20MHz bandwidth
Output OVP setpoint	31.5	32.7	33.9	Volts	25°C; recycle input voltage or PC to restart (>100 ms off)
Dissipation, standby		15.6	17.4	Watts	No load
Load regulation		±0.02	±0.2	%	No load to full load; nominal input
Output Current	0		14.29	Amps	
Current limit	14.5	16.4	19.4	Amps	Output voltage 95% of nominal
Short circuit current	10	16.4	19.4	Amps	Output voltage <250 mV

## 36 Vout, 400W (e.g. V72A36C400BL)

Parameter	Min	Тур	Max	Unit	Notes
Efficiency	86.0	88.0		%	Nominal input; full load; 25°C
Ripple and noise		200	250	mV	p-p; Nominal input; full load; 20MHz bandwidth
Output OVP setpoint	40.4	41.9	43.4	Volts	25°C; recycle input voltage or PC to restart (>100 ms off)
Dissipation, standby		15.3	16.0	Watts	No load
Load regulation		±0.02	±0.2	%	No load to full load; nominal input
Output Current	0		11.11	Amps	
Current limit	11.3	12.8	15	Amps	Output voltage 95% of nominal
Short circuit current	7.77	12.8	15	Amps	Output voltage <250 mV

## 48 Vout, 400W (e.g. V72A48C400BL)

Parameter	Min	Тур	Max	Unit	Notes
Efficiency	85.5	87.5		%	Nominal input; full load; 25°C
Ripple and noise		250	313	mV	p-p; Nominal input; full load; 20MHz bandwidth
Output OVP setpoint	53.7	55.7	57.7	Volts	25°C; recycle input voltage or PC to restart (>100 ms off)
Dissipation, standby		14.9	15.4	Watts	No load
Load regulation		±0.02	±0.2	%	No load to full load; nominal input
Output Current	0		8.33	Amps	
Current limit	8.49	9.58	11.3	Amps	Output voltage 95% of nominal
Short circuit current	5.83	9.58	11.3	Amps	Output voltage <250 mV



## **BASIC MODULE OPERATION**

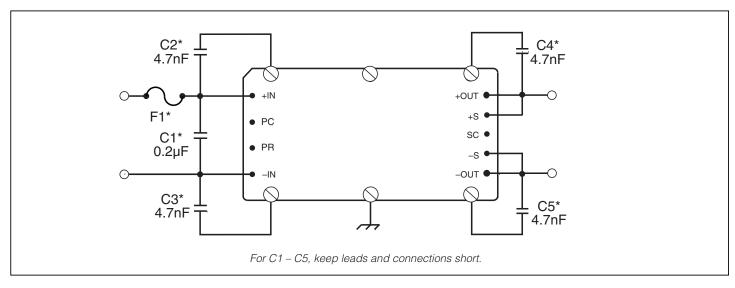


Figure 1 — Basic module operation requires fusing, grounding, bypassing capacitors.

## **Comprehensive Online Application Information**



The Design Guide and Applications Manual includes:

- Application circuits
- Design requirements
- EMC considerations
- Current sharing in power arrays
- Thermal performance information
- Recommended soldering methods
- Accessory modules filtering, rectification, front-ends
- Mounting options

...and more.

## CLICK HERE TO VIEW DESIGN GUIDE

Also at vicorpower.com

- PowerBench online configurators
- Over 20 Application Notes
- Online calculators thermal, trimming, hold-up
- PDF data sheets for ALL Vicor products



<sup>\*</sup> See Maxi, Mini, Micro Design Guide.

## PRIMARY CONTROL - PC PIN

#### Module Enable/Disable

The module may be disabled by pulling PC below 2.3 V with respect to the –Input. This may be done with an open collector transistor, relay, or optocoupler. Multiple converters may be disabled with a single transistor or relay either directly or via "OR'ing" diodes. See Figure 2.

#### **Primary Auxiliary Supply**

At 5.7 V, PC can source up to 1.5 mA. In the example shown in Figure 4, PC powers a module enabled LED.

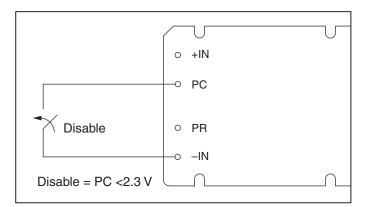


Figure 2 — Module enable/disable.

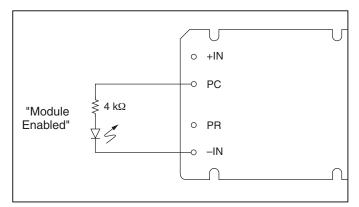


Figure 4 — LED on-state indicator.

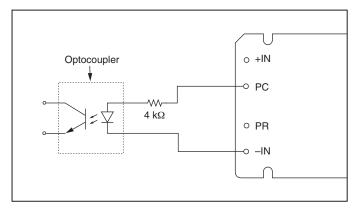
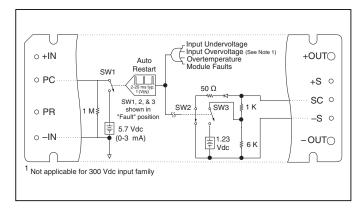


Figure 6 — Isolated on-state indicator.

#### Module Alarm

The module contains "watchdog" circuitry which monitors input voltage, operating temperature and internal operating parameters. In the event that any of these parameters are outside of their allowable operating range, the module will shut down and PC will go low. PC will periodically go high and the module will check to see if the fault (as an example, overtemperature) has cleared. If the fault has not been cleared, PC will go low again and the cycle will restart. The SC pin will go low in the event of a fault and return to its normal state after the fault has been cleared. See Figures 3 and 5.



*Figure 3* — *PC/SC module alarm logic.* 

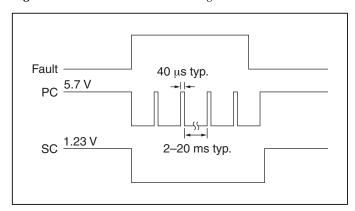
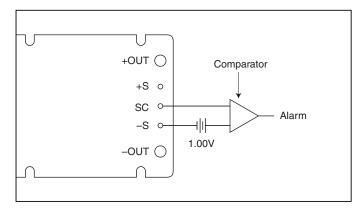


Figure 5 — PC/SC module alarm timing.



*Figure 7* — *Secondary side on-state indicator.* 



## SECONDARY CONTROL - SC PIN

#### **Output Voltage Programming**

The output voltage of the converter can be adjusted or programmed via fixed resistors, potentiometers or voltage DACs. See Figure 8.

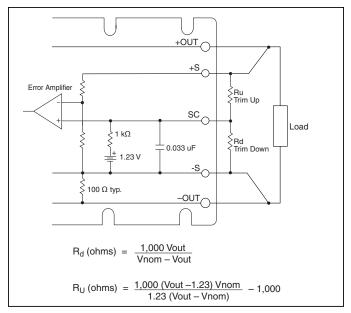


Figure 8 — Output voltage trim down and trim up circuit.

#### **Trim Down**

- 1. This converter is <u>not</u> a constant power device it has a constant current limit. Hence, available output power is reduced by the same percentage that output voltage is trimmed down. Do not exceed maximum rated output current.
- 2. The trim down resistor must be connected between the SC and -S pins. Do not bypass the SC pin directly with a capacitor.

#### **Trim Up**

- The converter is rated for a maximum delivered power. To
  ensure that maximum rated power is not exceeded, reduce
  maximum output current by the same percentage increase in
  output voltage.
- 2. The trim up resistor must be connected between the SC and +S pins. Do not bypass the SC pin directly with a capacitor.
- 3. Do not trim the converter above maximum trim range (typically +10%) or the output over voltage protection circuitry may be activated.

#### Trim resistor values calculated automatically:

On-line calculators for trim resistor values are available on the vicor website at:

asp.vicorpower.com/calculators/calculators.asp?calc=1
Resistor values can be calculated for fixed trim up, fixed trim down and for variable trim up or down.

## PARALLEL BUS - PR PIN

#### **Parallel Operation**

The PR pin supports paralleling for increased power with N+1 (N+M) redundancy. Modules of the same input voltage, output voltage, and power level will current share if all PR pins are suitably interfaced.

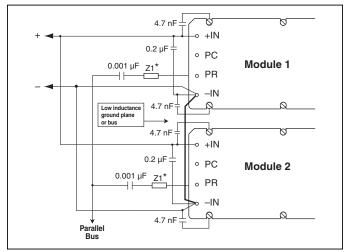
Compatible interface architectures include the following:

AC coupled single-wire interface. All PR pins are connected to a single communication bus through 0.001  $\mu$ F (500 V) capacitors. This interface supports current sharing and is fault tolerant except for the communication bus. Up to three converters may be paralleled by this method. See Figure 9.

*Transformer coupled interface.* For paralleling four or more converters a transformer coupled interface is required. See Figure 10.

For details on parallel operation please refer to the

Design Guide & Applications Manual for Maxi, Mini, Micro Family..



*Figure 9* — *AC coupled single-wire interface.* 

\* See Maxi, Mini, Micro Design Guide.

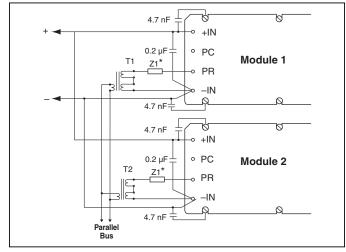
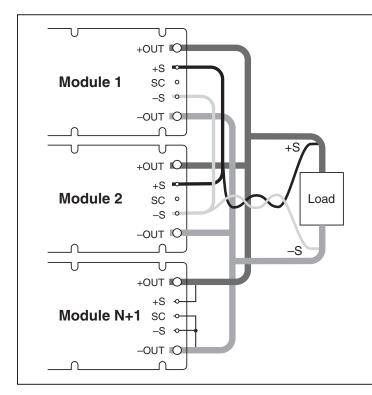


Figure 10 — Transformer-coupled interface.



## PARALLEL BUS OUTPUT



- The +Out and -Out power buses should be designed to minimize and balance parasitic impedance from each module output to the load.
- The +Sense pins must be tied together to form a +Sense bus. This must be Kelvin connected to +Out at a single point. The -Sense pins should be tied together to form a -Sense bus. This must be Kelvin connected to -Out at a single point.
- At the discretion of the power system designer, a subset of all modules within an array may be configured as slaves by connecting SC to -S.
- OR'ing diodes may be inserted in series with the +Out pins of each module to provide module output fault tolerance.
- The +Sense and -Sense leads should be routed in close proximity to each other on the printed circuit board. If wires are used to connect the converters on a PCB to an external load, the Sense leads should be twisted together to reduce noise pickup.

**Figure 11** — N+1 module array output connections.

#### **■ PIN STYLES\***

Designator	Description	Finish	Notes
(None)	Short	Tin/Lead	Requires in-board, mounting
L	Long	Tin/Lead	On-board mounting for 0.065" boards
S	Short ModuMate	Gold	SurfMate or in-board socket mounting
N	Long ModuMate	Gold	On-board socket mounting
F	Short RoHS	Gold	Select for RoHS compliant in-board solder, socket, or SurfMate mounting
G	Long RoHS	Gold	Select for RoHS compliant on-board solder or socket mounting
K	Extra Long RoHS	Gold	Select for RoHS compliance on-board mounting for thicker PCBs (not intended for socket or Surfmate mounting)

\* Pin style designator follows the "B" after the output power and precedes the baseplate designator.
Ex. V48A12T500BN2 — Long ModuMate Pins



## MECHANICAL DRAWINGS

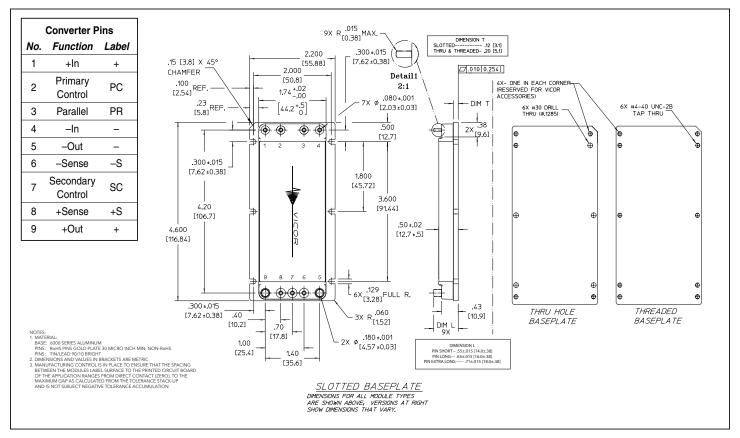


Figure 12 — Module outline

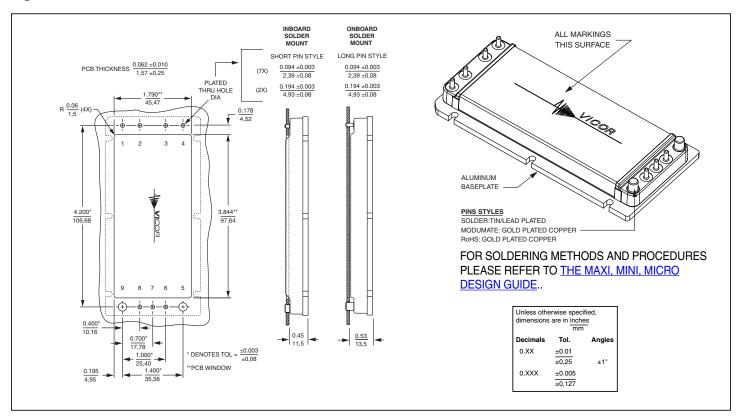


Figure 13 — PCB mounting specifications

# Vicor's comprehensive line of power solutions includes high density AC-DC and DC-DC modules and accessory components, fully configurable AC-DC and DC-DC power supplies, and complete custom power systems.

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Vicor will repair or replace defective products in accordance with its own best judgment. For service under this warranty, the buyer must contact Vicor to obtain a Return Material Authorization (RMA) number and shipping instructions. Products returned without prior authorization will be returned to the buyer. The buyer will pay all charges incurred in returning the product to the factory. Vicor will pay all reshipment charges if the product was defective within the terms of this warranty.

#### **Life Support Policy**

VICOR'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS PRIOR WRITTEN APPROVAL OF THE CHIEF EXECUTIVE OFFICER AND GENERAL COUNSEL OF VICOR CORPORATION. As used herein, life support devices or systems are devices which (a) are intended for surgical implant into the body, or (b) support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in a significant injury to the user. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system or to affect its safety or effectiveness. Per Vicor Terms and Conditions of Sale, the user of Vicor products and components in life support applications assumes all risks of such use and indemnifies Vicor against all liability and damages.

#### **Intellectual Property Notice**

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#### **Vicor Corporation**

25 Frontage Road Andover, MA, USA 01810 Tel: 800-735-6200 Fax: 978-475-6715

email

Customer Service: <u>custserv@vicorpower.com</u> Technical Support: <u>apps@vicorpower.com</u>

