Features

- Two High-side Outputs for Grounded Red Laser Diodes
- Total Output Current up to 700 mA at VLD = 3.8V
- One Low-side Output for Floating Blue Laser Diode
- Total Output Current up to 550 mA, External LD Supply Voltage
- Five Channels with Gain DACs
- Two Current Inputs for Read and Write Channels
- Channel 2 Current Contribution up to 350 mA
- LVDS-compatible Enable Inputs at Channels 2, 3, 4 and HFM
- Optional SyncDrive Technology for Increased Timing Accuracy
- Common Enable Input for All Channels
- Low-EMI High-Frequency Modulator (HFM) with Wiper
 - Frequency Range 200 MHz to 500 MHz
 - Swing up to 100 mApp
 - Erase Modulation Supported
- Serial Interface (SPI and TWI) with Write and Read Operation
 - LD Output
 - Channel Gains and Input Current Source
 - Overcurrent Protection
 - HFM Frequency and Swing
 - HFM Frequency Measurement
 - LVDS/Single Ended Input Configurations
 - SyncDrive Mode
 - Temperature Monitor
 - Blue LD Voltage Monitor
- Small, Pb-free, 24-pin, Paddle-up Package 4 mm \times 4 mm

Applications

- Blu-ray/DVD/CD Recordable Drives
- Blu-ray Combo Drives

Benefits

- The Serial Interface Yields the Following Benefits:
 - Low Pin Count and Small Package
 - Flexible Channel Configurations
 - Digital Gain Control
 - High Accuracy of HFM Frequency



3-output Laser Driver with 5 Channels and Serial Interface

ATR0881

Preliminary

Summary

NOTE: This is a summary document. The complete document is available. For more information, please contact your local Atmel sales office.

9105BS-DVD-03/08





1. Description

The ATR0881 is a laser diode driver designed to operate two grounded red laser diodes for CD and DVD and one floating blue laser diode for Blu-ray. The floating blue laser diode needs an extra supply voltage V_{H} . The serial interface (SPI and TWI supported) makes ATR0881 very flexible and configurable for a wide range of applications. Five channels can be assigned to one of the two reference current inputs IR and IW. The channel gains can be individually set by DACs. Prescalers with 2 bits are provided to increase the resolution. Overcurrent protection circuits supervise the reference current inputs. The channel enable inputs can be configured as single ended or LVDS. In SyncDrive mode, channels 3, 4, and 5 are synchronized to channel 2. The on-chip spread spectrum HFM with low EMI reduces laser mode hopping noise. All HFM frequency and swing parameters can be set by the serial interface. A high frequency-accuracy can be achieved by the frequency counter feature. HFM enable can be configured as single ended or LVDS or automatically-disabling by write channels.

2. Pin Configuration

Figure 2-1. Pinning QFN24

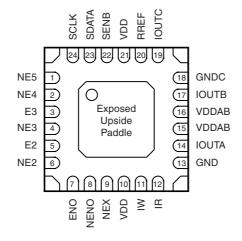


Table 2-1.Pin Description

Symbol	Туре	Function	
NE5	Digital in	Enable channel 5 (inverting, single ended) or enable channel 4 (non-inverting, LVDS)	
NE4	Digital in	Enable channel 4 (inverting, LVDS, or single ended)	
E3	Digital in	Enable channel 3 (non-inverting, LVDS)	
NE3	Digital in	Enable channel 3 (inverting, LVDS, or single ended)	
E2	Digital in	Enable channel 2 (non-inverting, LVDS)	
NE2	Digital in	Enable channel 2 (inverting, LVDS, or single ended)	
ENO	Digital in	Enable HFM (non-inverting, LVDS, or single ended)	
NENO	Digital in	Enable HFM (inverting, LVDS)	
NEX	Digital in	Multipurpose enable input (inverting, all channels, channel 1, write channels)	
VDD	Supply	Power supply	
IW	Analog in	Reference current input for write channels	
IR	Analog in	Reference current input for read channels	
GND	Supply	GND	
IOUTA	Analog out	Laser diode anode connection	
VDDAB	Supply	Power supply for outputs IOUTA and IOUTB	
IOUTB	Analog out	Laser diode anode connection	
GNDC	Supply	GND for output IOUTC	
IOUTC	Analog out	Laser diode cathode connection	
RREF	Analog in	Reference current input for HFM and overcurrent detector	
VDD	Supply	Power supply	
SENB	Digital in	Serial enable	
SDATA	Digital in/out	Serial data input and output	
SCLK	Digital in	Serial clock	
		Paddle – at least one corner has to be connected to GND	
	NE5 NE4 E3 NE3 E2 NE2 ENO NENO NEX VDD IW IR GND IOUTA VDDAB IOUTA VDDAB IOUTB GNDC IOUTC RREF VDD SENB SDATA	SymbolTypeNE5Digital inNE4Digital inE3Digital inE3Digital inE3Digital inE2Digital inE2Digital inE10Digital inNE2Digital inNE0Digital inNE0Digital inNENODigital inNENODigital inNENOSupplyIWAnalog inIRAnalog inIRAnalog outVDDABSupplyIOUTAAnalog outVDDABSupplyIOUTBAnalog outGNDCSupplyIOUTCAnalog outSENBDigital inVDDSupplySENBDigital in/out	





3. Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Parameters	Pin	Symbol	Value	Unit
Supply voltage	10, 15, 16, 21	V _{DD}	-0.3 to +6.0	v
Input voltage at any pin		V _{IN}	-0.3 to V _{DD} + 0.3	V
Current into IR, IW and RREF pins (no voltage source allowed)	11, 12, 20	I _{IN}	0 to 4	mA
Power dissipation		Р	0.7 ⁽¹⁾ to 1 ⁽²⁾	W
Junction temperature		Т _ј	150	°C
Storage temperature		T _{stg}	-65 to +125	°C

Notes: 1. $R_{thJA} \leq 115^{\circ}C/W$ at $T_{amb} = 70^{\circ}C$

2. $R_{thJA} \leq 115^{\circ}C/W$ at $T_{amb} = 25^{\circ}C$

4. Thermal Resistance

Parameters	Symbol	Value	Unit
Junction-ambient	R _{thJA}	50	°C/W

Note: Package thermal resistance is measured on the ATR0881 demo board without heat spreader. Users should ensure that the junction temperature never exceeds 150°C. Thermal resistance from junction to case and to ambient depends very much on the PCB layout and on heat spreading measures.

5. Recommended Operating Conditions

Parameters	Pin	Symbol	Value	Unit
Supply voltage	10, 15, 16, 21	V _{DD}	4.5 to 5.5	V
Input current	11, 12	I _R , I _W	≤ 2	mA
External reference resistor to GND	20	R _{REF}	13 ±1%	kΩ
Operating voltage at IOUTC output	19	V _{IOUTC}	0.8 to 4.0 ⁽¹⁾	V
Operating temperature range		T _{amb}	0 to +70	°C

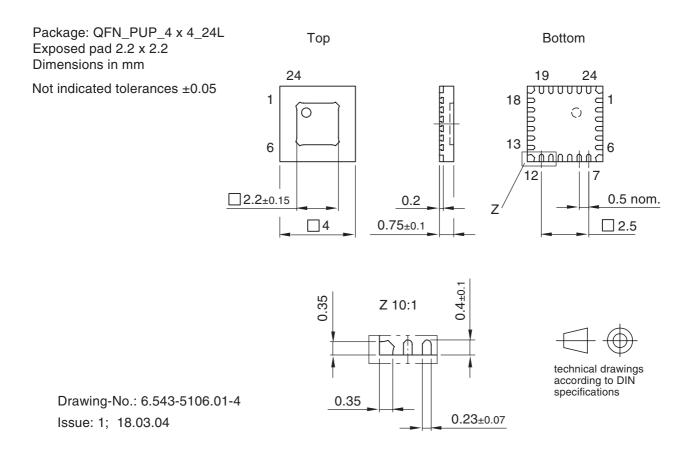
Note: 1. Adjust supply voltage V_H of blue laser diode to keep V_{IOUTC} within the specified range for all operating modes of the laser diode to avoid degradation of the laser diode.

ATR0881 [Preliminary]

6. Ordering Information

Extended Type Number	Package	Remarks
ATR0881-PTQW	QFN24_PUP	Taped and reeled

7. Package Information



8. Revision History

Please note that the following page numbers referred to in this section refer to the specific revision mentioned, not to this document.

Revision No.	History
	All TWI parts added
9105BS-DVD-03/08	 Applications on page 1 changed
	 Section 4 "Thermal Resistance" on page 4 changed





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