

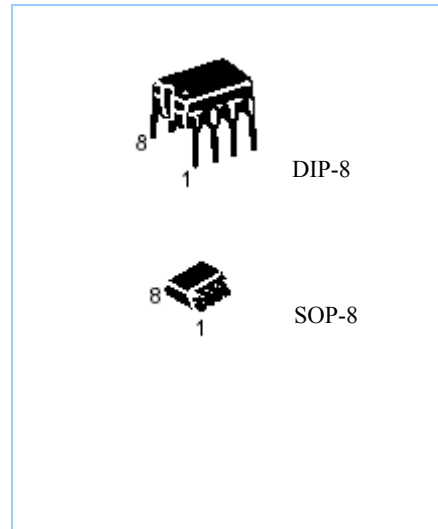
1.5A, Step-Up/Down Inverting Switching Regulators

DESCRIPTION

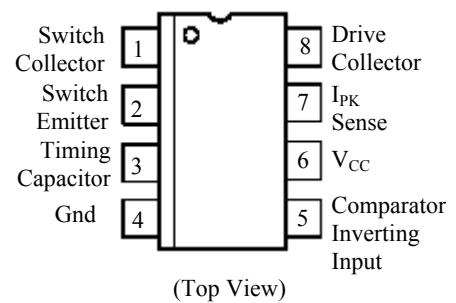
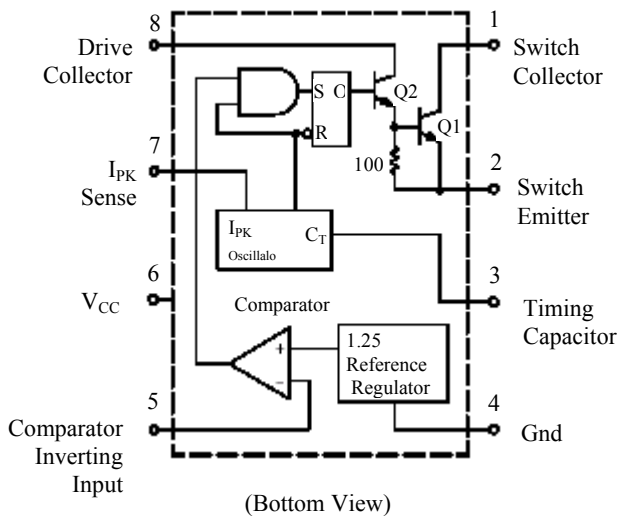
The AV34063 is a monolithic control circuit containing the primary functions required for DC-to-DC converters. This device consists of an internal temperature compensated reference, comparator, controlled duty cycle oscillator with an active current limit circuit, driver and high current output switch. This device was specifically designed to be incorporated in Step-Down and Step-Up and Voltage-inverting applications with a minimum number of external components.

FEATURES

- Operation from 3.0 to 40V input
- Low standby current
- Current limiting
- Output switch current of 1.5A
- Output voltage adjustable
- Frequency of operation from 100Hz to 100KHz
- Precision 2% reference



BLOCK DIAGRAM



ORDERING INFORMATION

Part Number	Operating Temperature Range	Package Type
AV34063	0°C ~+70°C	DIP-8
AV34063S	0°C ~+70°C	SOP-8

MAXIMUM RATING

Characteristic	Symbol	Value	Unit
Power Supply Voltage	V _{CC}	40	V
Comparator Input Voltage Range	V _{IR}	-0.3 to +40	
Switch Collector Voltage	V _{C(switch)}	40	
Switch Emitter Voltage (V _{PINI} =40V)	V _{E(switch)}	40	
Switch Collector to Emitter Voltage	V _{CE(switch)}	40	
Driver Collector Voltage	V _{C(driver)}	40	
Driver Collector Current (Note1)	I _{C(driver)}	100	mA
Switch Current	I _{SW}	1.5	A
Power Dissipation and Thermal Characteristics Plastic Package T _A =25°C	P _D	1.25	W
Thermal Resistance	R _{θJA}	100	°C/ W
SOIC Package T _A =25°C	P _D	0.625	W
Thermal Resistance	R _{θJA}	100	°C/ W
Operating Junction Temperature	T _J	+150	°C
Operating Ambient Temperature Range	T _A	0 to +70	°C
Storage Temperature Range	T _{stg}	-60~150	°C

Notes: 1. Maximum package power dissipation limits must be observed.

2.ESD data available upon request.

ELECTRICAL CHARACTERISTICS(V_{CC}=5.0V, T_A=T_{low} to T_{high} [Note1], unless otherwise specified.)

Characteristics	Symbol	Min	Typ	Max	Unit
OSCILLATOR					
Frequency (V _{pin5} =0V, C _T =1.0nF, T _A =25°C)	F _{OSC}	24	33	42	KHz
Charge Current (V _{CC} =5.0V to 40V, T _A =25°C)	I _{chg}	24	35	42	μA
Discharge Current (V _{CC} =5.0V to 40V, T _A =25°C)	I _{dischg}	140	220	260	μA
Discharge to Charge Current Ratio (Pin 7 to V _{CC} , T _A =25°C)	I _{dischg} / I _{chg}	5.2	6.5	7.5	-
Current Limit Sense Voltage (I _{chg} = I _{dischg} , T _A =25°C)	V _{ipk(sence)}	250	300	350	mV
OUTPUT SWITCH (NOTE 2)					
Saturation Voltage, Darlington Connection (Note 3) (I _{SW} =1.0A, Pins 1,8 connected)	V _{CE(sat)}	-	1.0	1.3	V
Saturation Voltage, Darlington Connection (I _{SW} =1.0A, R _{pin8} =82Ω to V _{CC} , Forced β≈20)	V _{CE(sat)}	-	0.45	0.7	V
DC Current Gain (I _{SW} =1.0A, V _{CE} =5.0V, T _A =25°C)	H _{EF}	50	75	-	-
Collector Off-State Current (V _{CE} =40V)	I _{C(off)}	-	0.01	100	μA
COMPARATOR					
Threshold Voltage (T _A =25°C) (T _A = T _{low} to T _{high})	V _{th}	1.238 1.225	1.25 -	1.262 1.275	V
Threshold Voltage Line Regulation (V _{CC} =3.0V to 40V)	Reg _{line}	-	1.4	5.0	mV
Input Bias Current (V _{in} =0V)	I _{IB}	-	-20	-400	nA
TOTAL DEVICE					
Supply Current ((V _{CC} =5.0V to 40V, C _T =1.0nF, (Pin 7 to V _{CC} , (Pin 5> V _{th} (Pin 2=Gnd, remaining pins open)	I _{CC}	-	-	4.0	mA

Notes: 1. T_{low}=0°C T_{high}=+70°C

- Low duty cycle pulse techniques are used during test to maintain junction temperature as close to ambient temperature as possible.
- If the output switch is driven into hard saturation (non-Darlington) at low switch current (≤300mA) and high driver currents (≥30mA), it may take up to 2.0 μs for it to come out of saturation. This condition will shorten the off time at frequencies ≥30KHz, and is magnified at high temperature. This condition does not occur with a Darlington configuration, since the output switch cannot saturate. If a non-Darlington configuration is used, the following output drive condition is recommended :

Forced β of output switch : (I_C output/ (I_C driver - 7.0mA) ≥10)

*The 100Ω resistor in the emitter of the driver device requires about 7.0mA before the output switch conducts.

Typical Characteristics

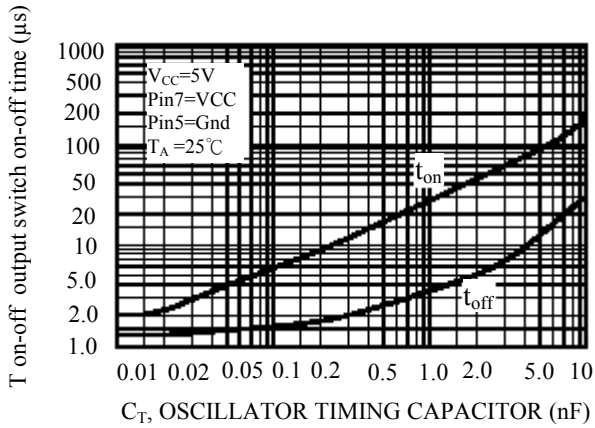


Figure 1. Output Switch On-Off Time versus Oscillator Timing Capacitor

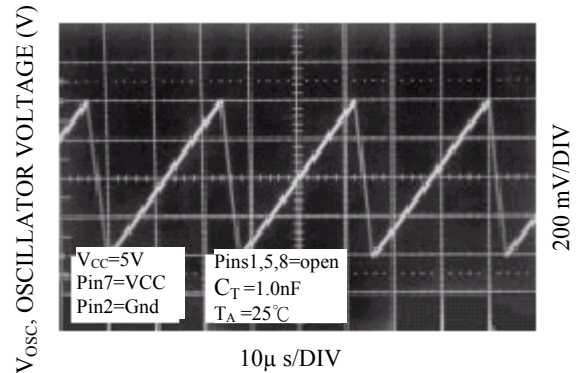


Figure2. Timing Capacitor Waveform

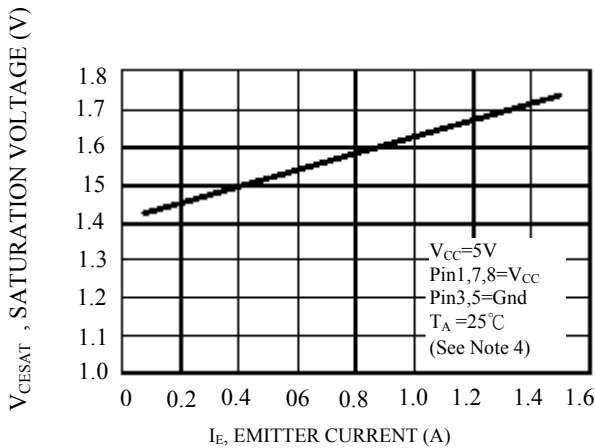


Figure 3. Emitter Follower Configuration Output Saturation Voltage versus Emitter

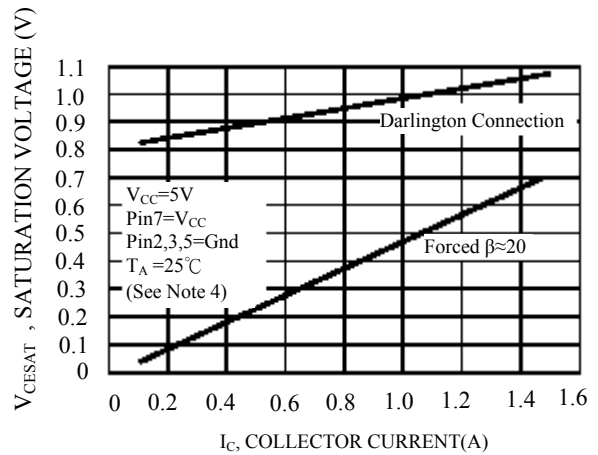


Figure 4. Common Emitter Configuration Output Switch Saturation Voltage versus Collector Current

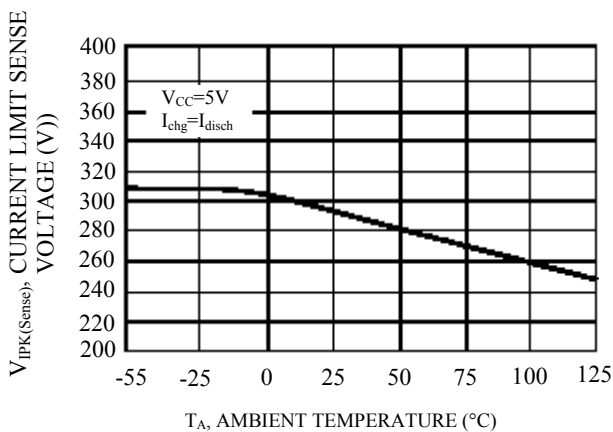


Figure 5. Current Limit Sense Voltage versus Temperature

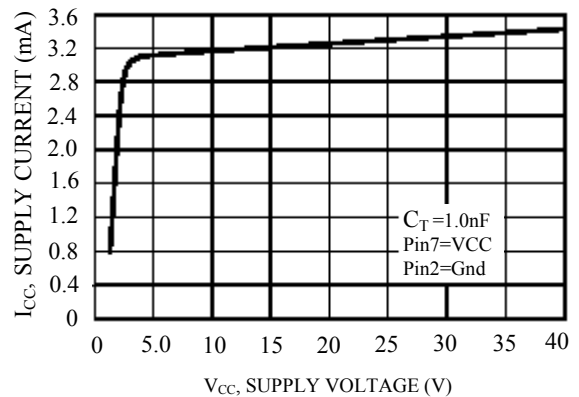
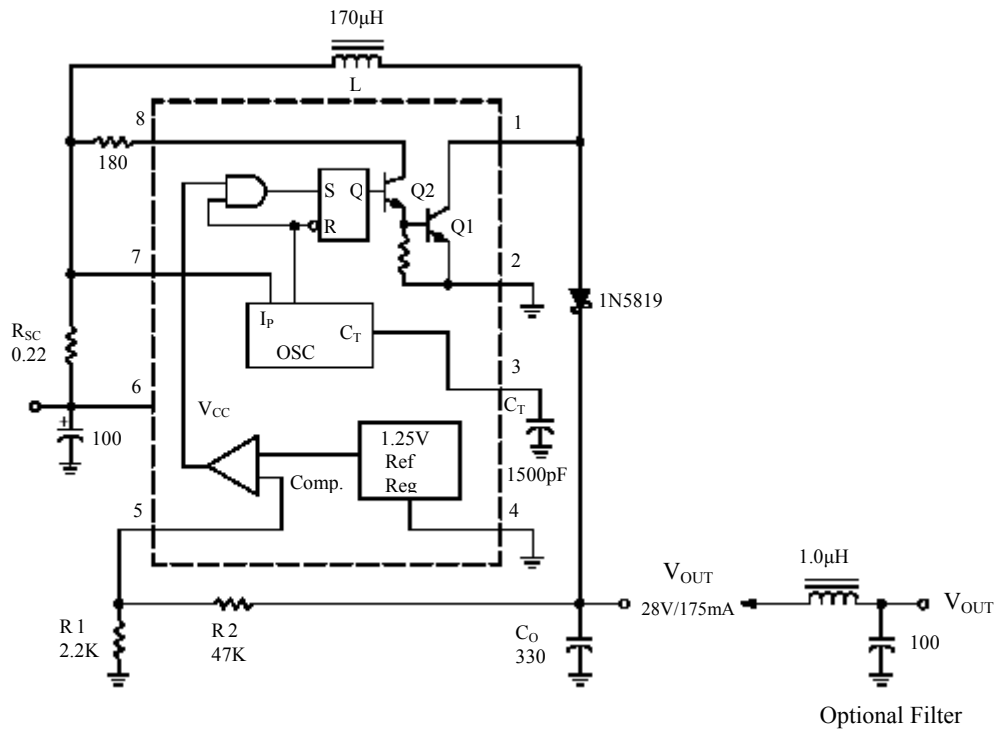


Figure 6. Standby Supply Current versus Supply Voltage



Test	Conditions	Results
Line Regulation	$V_{in} = 8.0 \text{ V to } 16 \text{ V}, I_O = 175 \text{ mA}$	$30 \text{ mV} = \pm 0.05\%$
Load Regulation	$V_{in} = 12 \text{ V}, I_O = 75 \text{ mA to } 175 \text{ mA}$	$10 \text{ mV} = \pm 0.017\%$
Output Ripple	$V_{in} = 12 \text{ V}, I_O = 175 \text{ mA}$	400 mVpp
Efficiency	$V_{in} = 12 \text{ V}, I_O = 175 \text{ mA}$	87.7%
Output Ripple With Optional Filter	$V_{in} = 12 \text{ V}, I_O = 175 \text{ mA}$	40 mVpp

Figure 7. Step-Up Converter

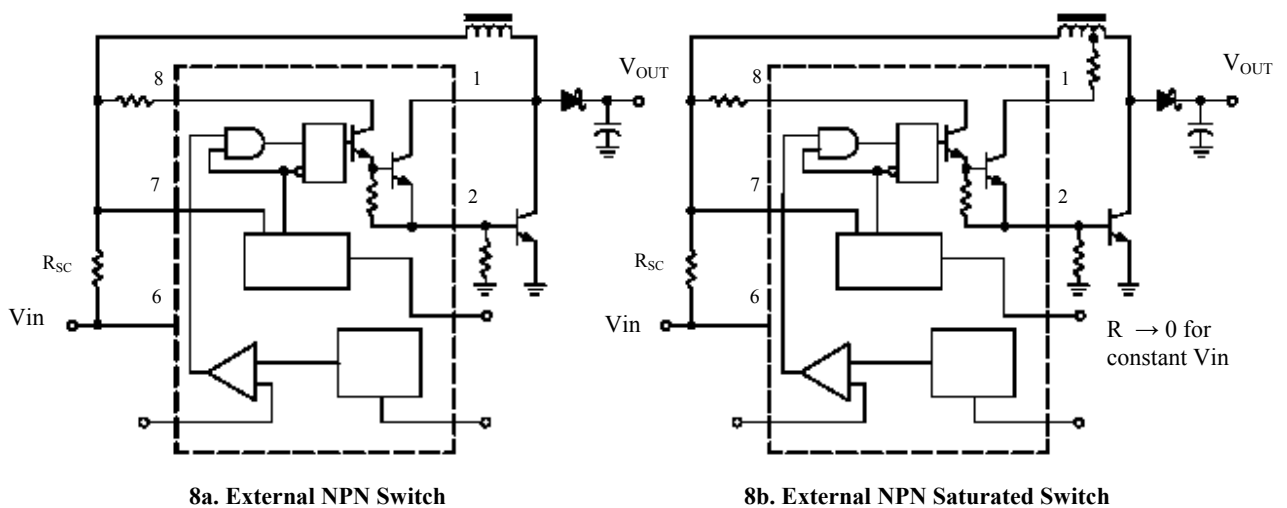
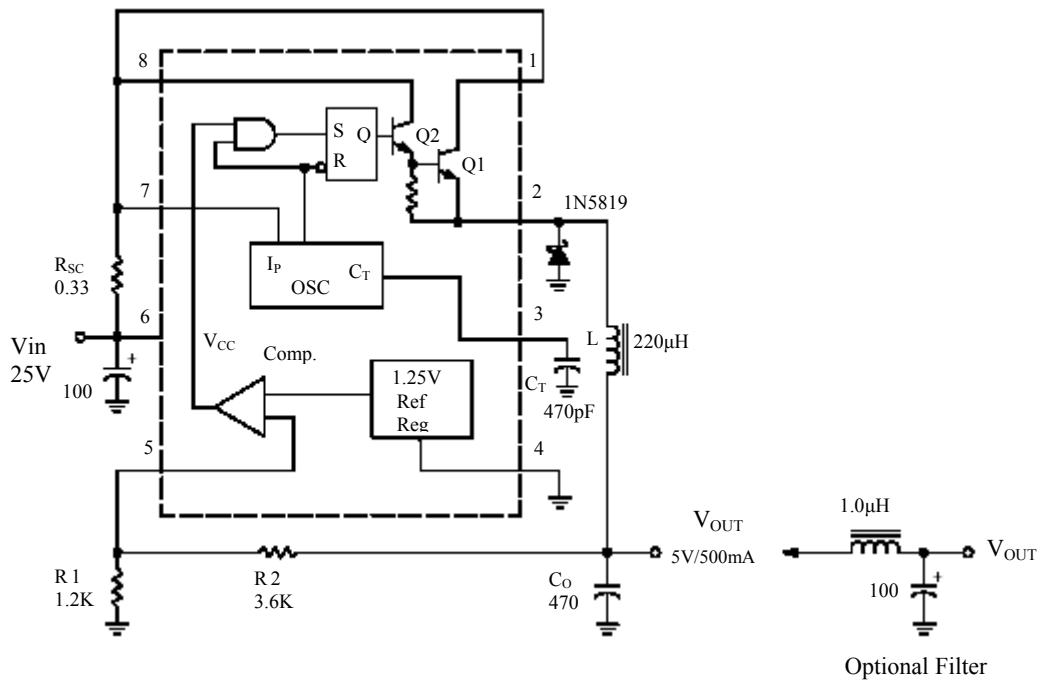
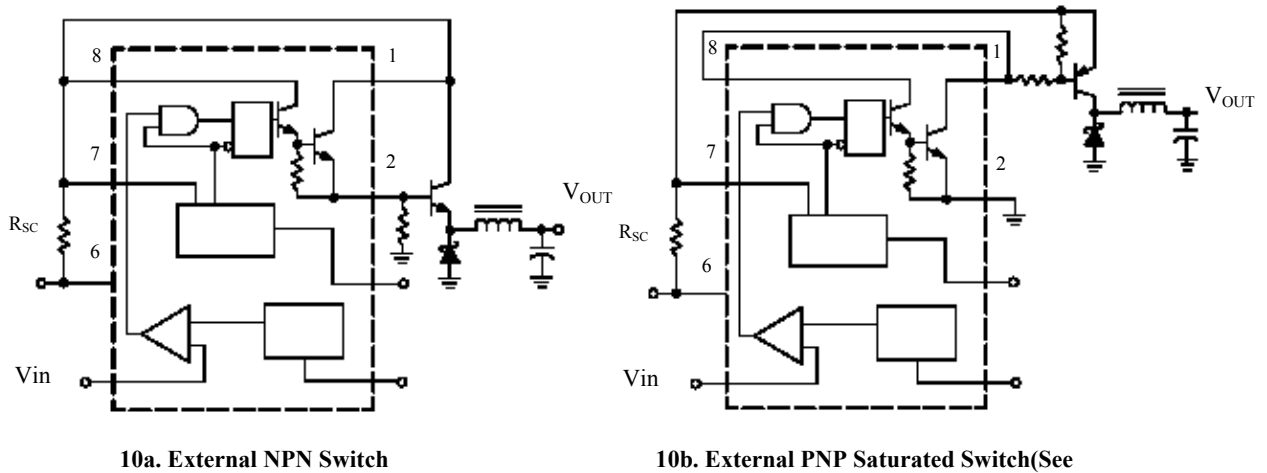


Figure 8. External Current Boost Connections for I_C Peak Greater than 1.5 A



Test	Conditions	Results
Line Regulation	$V_{in} = 15\text{ V to }25\text{ V}, I_O = 500\text{ mA}$	$12\text{ mV} = \pm 0.12\%$
Load Regulation	$V_{in} = 25\text{ V}, I_O = 50\text{ mA to }500\text{ mA}$	$3.0\text{ mV} = \pm 0.03\%$
Output Ripple	$V_{in} = 25\text{ V}, I_O = 500\text{ mA}$	120 mV_{pp}
Short Circuit Current	$V_{in} = 25\text{ V}, R_L = 0.1\Omega$	1.1 A
Efficiency	$V_{in} = 25\text{ V}, I_O = 500\text{ mA}$	83.7%
Output Ripple With Optional Filter	$V_{in} = 25\text{ V}, I_O = 500\text{ mA}$	40 mV_{pp}

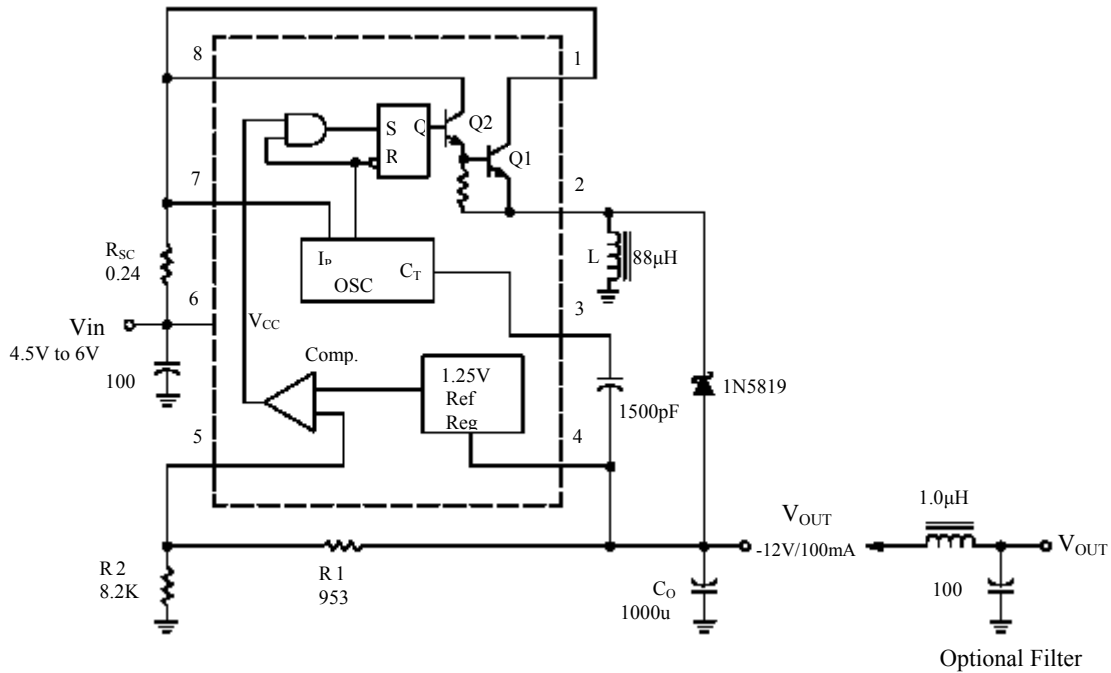
Figure9. Step-Down Converter



10a. External NPN Switch

10b. External PNP Saturated Switch(See

Figure10. External Current Boost Connections for I_C Peak Greater than 1.5 A



Test	Conditions	Results
Line Regulation	Vin = 4.5 V to 6 V, IO = 100 mA	3.0 mV = ±0.012%
Load Regulation	Vin = 5.0 V, IO = 10 mA to 100 mA	0.022 mV = ±0.09%
Output Ripple	Vin = 5.0 V, IO = 100 mA	500 mVpp
Short Circuit Current	V in = 5.0 V, RL = 0.1Ω	910mA
Efficiency	Vin = 5.0 V, IO = 100 mA	62.6%
Output Ripple With Optional Filter	Vin = 5.0 V, IO = 100 mA	70 mVpp

Figure11. Step-Down Converter

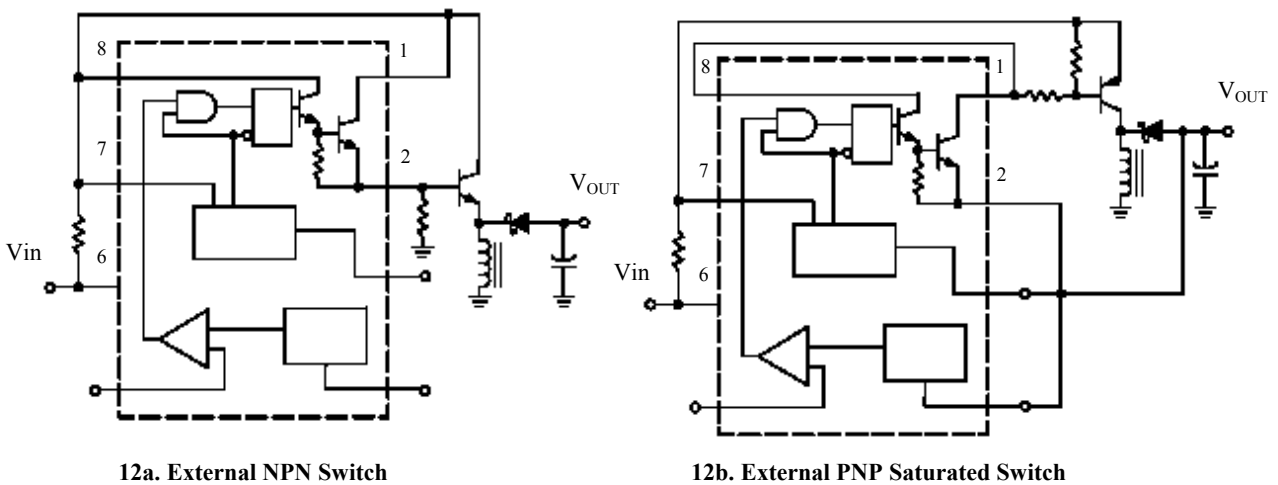


Figure12. External Current Boost Connections for IC Peak Greater than 1.5 A

Note 5 : If the output switch is driven into hard saturation (non-Darlington configuration) at low switch currents ($\leq 300\text{mA}$) and high driver currents ($\geq 30\text{mA}$), it may take up to $2.0\mu\text{s}$ to come out of saturation. This condition will shorten the off time at frequencies $\geq 30\text{KHz}$, and is magnified at high temperatures. This condition does not occur with a darlington configuration, since the output switch cannot saturate. If a non-darlington is used, the following output drive condition is recommended.

Calculation	Step-Up	Step-Down	Voltage-Inverting
$t_{on}t_{off}$	$\frac{V_{out} + V_F - V_{in(min)}}{V_{in(min)} - V_{sat}}$	$\frac{V_{out} + V_F}{V_{in(min)} - V_{sat} - V_{out}}$	$\frac{ V_{out} + V_F}{V_{in} - V_{sat}}$
$(t_{on} + t_{off})$	$\frac{1}{f}$	$\frac{1}{f}$	$\frac{1}{f}$
t_{off}	$\frac{t_{on} + t_{off}}{\frac{t_{on}}{t_{off}} + 1}$	$\frac{t_{on} + t_{off}}{\frac{t_{on}}{t_{off}} + 1}$	$\frac{t_{on} + t_{off}}{\frac{t_{on}}{t_{off}} + 1}$
t_{on}	$(t_{on} + t_{off}) - t_{off}$	$(t_{on} + t_{off}) - t_{off}$	$(t_{on} + t_{off}) - t_{off}$
C_T	$4.0 \times 10^{-5} t_{on}$	$4.0 \times 10^{-5} t_{on}$	$4.0 \times 10^{-5} t_{on}$
$I_{pk(switch)}$	$2I_{out(max)} \left(\frac{t_{on}}{t_{off}} + 1 \right)$	$2I_{out(max)}$	$2I_{out(max)} \left(\frac{t_{on}}{t_{off}} + 1 \right)$
R_{sc}	$0.3/I_{pk(switch)}$	$0.3/I_{pk(switch)}$	$0.3/I_{pk(switch)}$
$L_{(min)}$	$\left(\frac{(V_{in(min)} - V_{sat})}{I_{pk(switch)}} \right) t_{on(max)}$	$\left(\frac{(V_{in(min)} - V_{sat} - V_{out})}{I_{pk(switch)}} \right) t_{on(max)}$	$\left(\frac{(V_{in(min)} - V_{sat})}{I_{pk(switch)}} \right) t_{on(max)}$
C_O	$g \frac{I_{out}t_{on}}{V_{ripple(pp)}}$	$\frac{I_{pk(switch)}(t_{on} + t_{off})}{8V_{ripple(pp)}}$	$g \frac{I_{out}t_{on}}{V_{ripple(pp)}}$

Figure13. Design Formula Table

V_{sat} =Saturation voltage of the output switch.

V_F =Forward voltage drop of the output rectifier.

The following power supply characteristics must be chosen:

V_{in} -Nominal input voltage.

V_{out} -Desired output voltage, $V_{out} = 1.25(1+R2/R1)$.

I_{out} - Desired output current.

F_{min} -Minimum desired output switching frequency at the selected value of V_{in} and I_o .

$V_{ripple(p-p)}$ - Desired peak-to-peak output ripple voltage. In practice, the calculated capacitor value will need to be increased due to its equivalent series resistance and board layout. The ripple voltage should be kept to a low value since it will directly affect the line and load regulation.