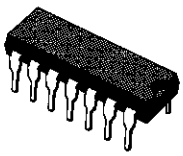
SGS-THOMSON MICROELECTRONICS

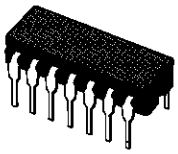
HCC/HCF4006B

18-STAGE STATIC SHIFT REGISTER

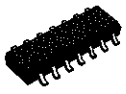
- PERMANENT REGISTER STORAGE WITH CLOCK LINE "HIGH" OR "LOW" ... NO INFORMATION RECIRCULATION REQUIRED
- FULLY STATIC OPERATION
- SHIFTING RATES UP TO 12MHz @ 10V (typ.)
- STANDARDIZED SYMMETRICAL OUTPUT CHARACTERISTICS
- QUIESCENT CURRENT SPECIFIED TO 20V FOR HCC DEVICE
- INPUT CURRENT OF 100nA AT 18V AND 25 °C FOR HCC DEVICE
- 100% TESTED FOR QUIESCENT CURRENT
- 5V, 10V, AND 15V PARAMETRIC RATING
- MEETS ALL REQUIREMENTS OF JEDEC TENTATIVE STANDARD N° 13A, "STANDARD SPECIFICATIONS FOR DESCRIPTION OF "B" SERIES CMOS DEVICES"



EY
(Plastic Package)



F
(Ceramic Frit Seal Package)



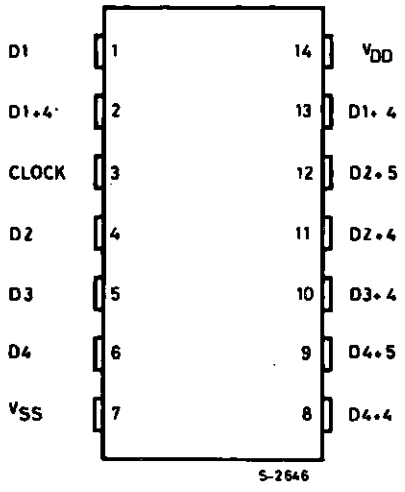
M1
(Micro Package)

C1
(Plastic Chip Carrier)

ORDER CODES :

HCC4006BF HCF4006BM1
HCF4006BEY HCF4006BC1

PIN CONNECTIONS



DESCRIPTION

The **HCC4006B** (extended temperature range) and the **HCF4006B** (standard temperature range), are monolithic integrated circuits, available in 14-lead dual in-line plastic or ceramic package and plastic micro package. The types are comprised of 4 separate "shift register" sections ; two sections of four stages and two sections of five stages with an output tap at the fourth stage. Each section has an independent "single rail" data path. A common clock signal is used for all stages. Data is shifted to the next stage on negative-going transitions of the clock. Through appropriate connections of inputs and outputs, multiple register sections of 4, 5, 8, and 9 stages or single register sections of 10, 12, 13, 14, 16, 17 and 18 can be implemented using one **HCC/HCF4006B** package. Longer shift register sections can be assembled by using more than one **HCC/HCF4006B**. To facilitate cascading stages when clock rise and fall times are slow, an optional output ($D_1 + 4'$) that is delayed one-half clock-cycle, is provided (see truth table for output from pin 2).