

# AZ DISPLAYS, INC.

---

*COMPLETE LCD SOLUTIONS*

## SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

PART NUMBER:

AGM3224I-1 Series

REVISED:

MARCH 13, 2007

## LCD Module

### Main Data

No.	Item	Contents	Unit
(1)	Module size	167.0(W) x 109.0(H) x 8.5 (D)	mm
(2)	Viewing area	120.0 (W) x 90.0 (H)	mm
(3)	Dot Number	320 x 3 (R.G.B) (W) x 240 (H)	dots
(4)	Dot Size	0.10(W) x 0.34(H)	mm
(5)	Dot pitch	0.12(W) x 0.36(H)	mm
(6)	LCD type	<ul style="list-style-type: none"> <li>•Color-STN (Negative &amp; transmissive type)</li> <li>• with Anti glare upper polarizer .</li> </ul>	-
(7)	Contrast ratio	40	-
(8)	Duty	1/242	-
(9)	Viewing direction	6 O'clock	-
(10)	Operating temperature	0 ~ +50	°C
(11)	Storage temperature	-20 ~ +60	°C
(12)	Backlight	LED x 16	pcs
(13)	Power Supply Voltage	VDD=5.0 V or 3.3 V VLCD=22.5 V	V
(14)	Weight	200 (Approx.)	g

## Interface Pin Connection

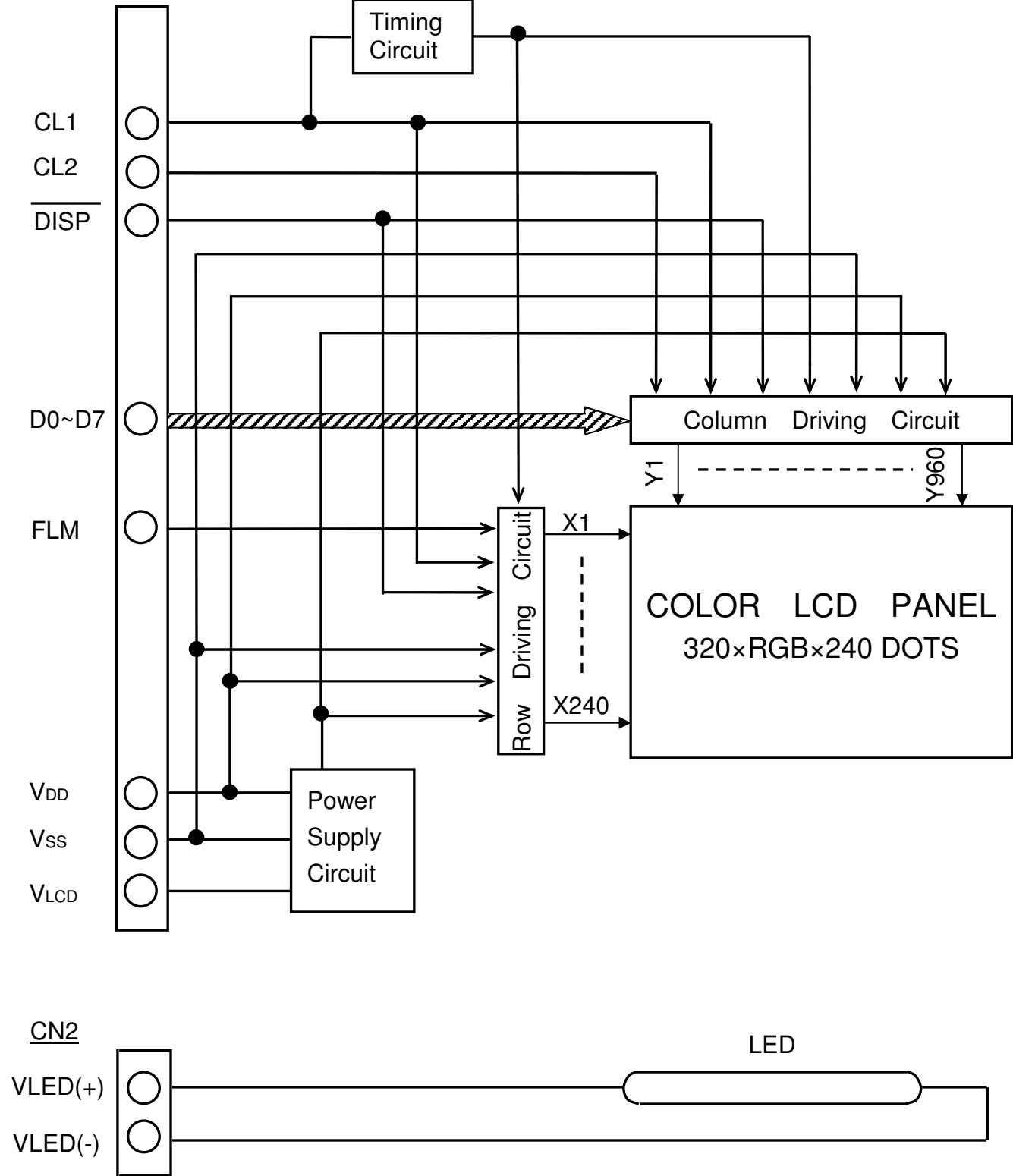
CN1 : FFC : Pitch 1.0mm, width 17.0mm.

Pin No.	Signal	Pin Function
1	FLM	First Line Marker
2	CL1	Input data latch signal (LOAD)
3	CL2	Data shift clock (CP)
4	<u>DISP OFF</u>	Display control signal H:ON L:OFF
5	VDD	Power supply for Logic
6	VSS	GND
7	VLCD	Power supply for LCD
8	D0	Display data
9	D1	Display data
10	D2	Display data
11	D3	Display data
12	D4	Display data
13	D5	Display data
14	D6	Display data
15	D7	Display data
16	VSS	GND

CN2 : M63-M83-04 (MITSUMI)

Pin No.	Signal	Function
1	LED(+)	Power supply voltage for LED
2	-	-----
3	-	-----
4	LED(-)	LED GND

# Block Diagram



## Maximum Ratings

### Electrical Absolute Maximum Ratings. (LCM) (VSS=0V)

Item	Symbol	Min.	Max.	Unit
Power supply for Logic	V <sub>DD</sub> -V <sub>SS</sub>	-0.3	7.0	V
Contrast Adjustment Voltage	V <sub>LCD</sub> -V <sub>SS</sub>	0	45	V
Input voltage (Note 1)	V <sub>i</sub>	-0.3	V <sub>DD</sub> +0.3	V

Note 1. FLM,CL1,CL2,DISP,D0~D7.

Note 2. Ta=25°C

Note 3. Make certain you are grounded when handling LCM.

### Environmental Absolute Maximum Ratings

Item	Operating		Storage		Remark
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	0°C	50°C	-20°C	60°C	Note1,2,3
Humidity	Note 4		Note 4		No Condensation
Vibration	-	2.45m/s <sup>2</sup>	-	11.76 m/s <sup>2</sup> Note 5	1h max Note 6
Shock	-	29.4m/s <sup>2</sup>	-	490 m/s <sup>2</sup> Note 5	XYZ directions 11ms

Note 1. Ta at -20°C -----<48hours, at 60°C -----<120 hours.

Note 2. Background color changes slightly depending on ambient temperature.  
The phenomenon is reversible.

Note 3. Ta≤40°C : 85%RH MAX.

Ta> 40°C : Absolute humidity must be lower than the humidity of 85% RH at 40°C.

Note 4. The module should be operated normally after the test is finished.

Note 5. 5Hz ~ 100Hz (Except resonance frequency).

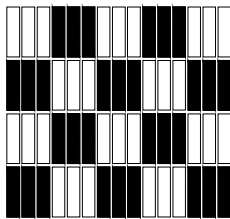
# Electrical Characteristics

## Electrical Characteristics of LCD

Item	Symbol	Condition	MIN.	Typ.	Max.	Unit	
Power Supply for Logic	$V_{DD}$	$V_{DD}-V_{SS}$	3.15	5.0	5.5	V	
Input Signal Voltage Note (1)	$V_{IH}$	“H” Level	$0.8V_{DD}$	—	$V_{DD}$	V	
	$V_{IL}$	“L” Level	0	—	$0.2V_{DD}$		
Power supply current Logic	$I_{DD}$	$V_{DD}=5.0V$	—	2.2	5.0	mA	
		$V_{DD}=3.3V$	—	1.0	5.0	mA	
Power supply current Lcd	$I_{EE}$	$V_{LCD}=22.5V$ , Note (2)	—	6.5	13.0	mA	
Recommended LCD Driving Voltage	$V_{LCD}-V_{SS}$	Duty=1/242 Bias=1/15	$T_a=0^{\circ}C$	—	(23.6)	—	V
			$T_a=25^{\circ}C$	22.0	22.5	23.0	
			$T_a=50^{\circ}C$	—	(21.4)	—	
Frame Frequency	fFLM	—	130	150	170	Hz	

Note (1) FLM,CL1,CL2,DISP,D0~D7.

Note (2) fFLM=150Hz, $T_a=25^{\circ}C$ ,Display pattern is Black/White cross pattern as below.



## Electrical Characteristics of Backlight

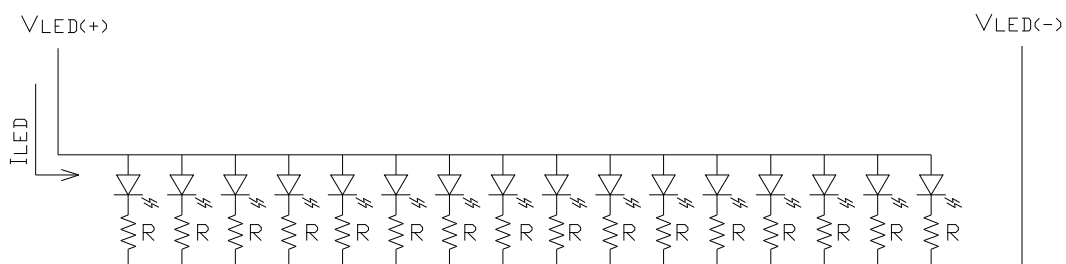
Item	Symbol	Min.	Typ.	Max.	Unit	Condition
Voltage	$V_{LED}$	(4.2)	(4.3)	(4.4)	V	Note 1
Current	$I_{LED}$	-	288.0	-	mA	Note 2
Number of LED	-	-	16	-	EA	-
Power Consumption	-	-	1.24	-	W	Note 3

Note (1):  $V_{LED} = V_{LED(+)} - V_{LED(-)}$  .

Note (2): The current of LED is 18 mA for each one.

LED driving in constant current mode is recommended .

Note (3): LED power consumption is around 0.0775W for each one.



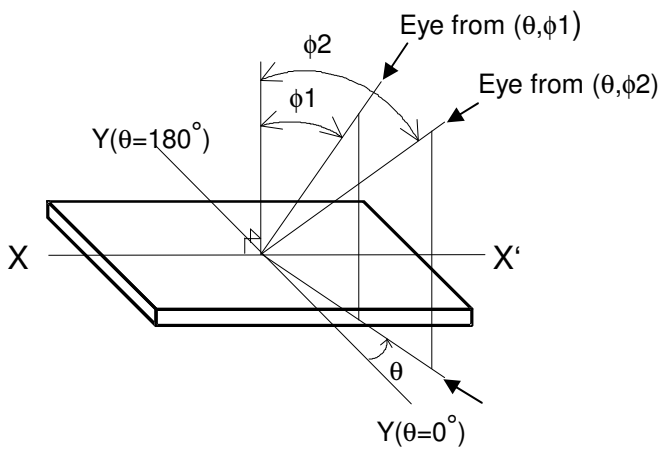
# Optical Characteristics

## Optical Characteristics of LCD

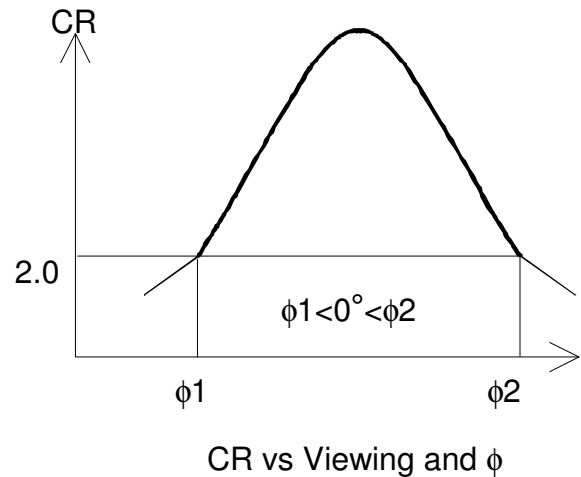
Ta= 25°C.(Backlight On)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	
Viewing Angle Range	$\phi 1, \phi 2$	$\theta=0^\circ K \geq 2$	-	(40)	-	Deg.	Note 1,2	
Contrast Ratio	K	$\theta=0^\circ, \phi=0^\circ$	20	40	-	-	Note 2	
Response Time	Rise	$\theta=0^\circ, \phi=0^\circ$	-	(250)	-	ms	Note 2	
	Fall	$\theta=0^\circ, \phi=0^\circ$	-	(200)	-	ms		
Color Tone (CIE Coordinate)	R	x	$\theta=0^\circ, \phi=0^\circ$	(0.44)	(0.49)	(0.54)	-	-
		y		(0.26)	(0.31)	(0.36)	-	-
	G	x		(0.26)	(0.31)	(0.36)	-	-
		y		(0.49)	(0.54)	(0.59)	-	-
	B	x		(0.10)	(0.15)	(0.20)	-	-
		y		(0.07)	(0.12)	(0.17)	-	-
	W	x		(0.21)	(0.26)	(0.31)	-	-
		y		(0.25)	(0.30)	(0.35)	-	-

**Note 1. Definition of  $\theta$  and  $\phi$**



**Note 2. Definition of Viewing angle  $\phi 1$  and  $\phi 2$**



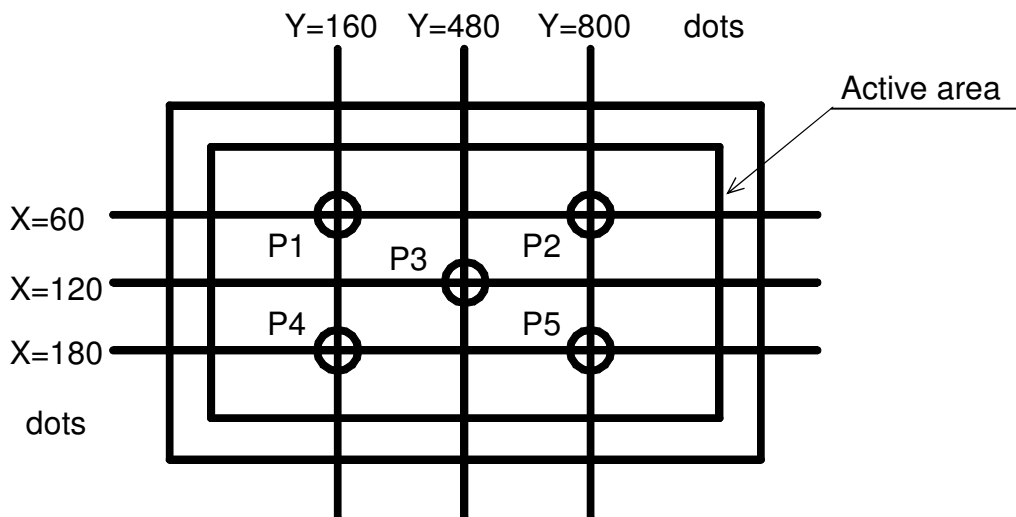
### Optical Characteristics of Backlight

Item	Min.	Typ.	Max.	Unit	Remark
Brightness	100	150	-	cd/m <sup>2</sup>	Note 1,2,3
Brightness Uniformity	-	-	±30	%	Note 2,3,4

Note 1. Measurement Condition:

- Display data should be all “ON” (D0~D7=HIGH).
- VDD=5.0V, VLED=4.3V, ILED=288mA, VLCD should be adjusted at the voltage where the peak contrast is obtained by naked eyes as the “All Q” pattern.

Note 2. Measurement of the following 5 points on the display.



Note3. The brightness shall be the average of P1~P5 point.

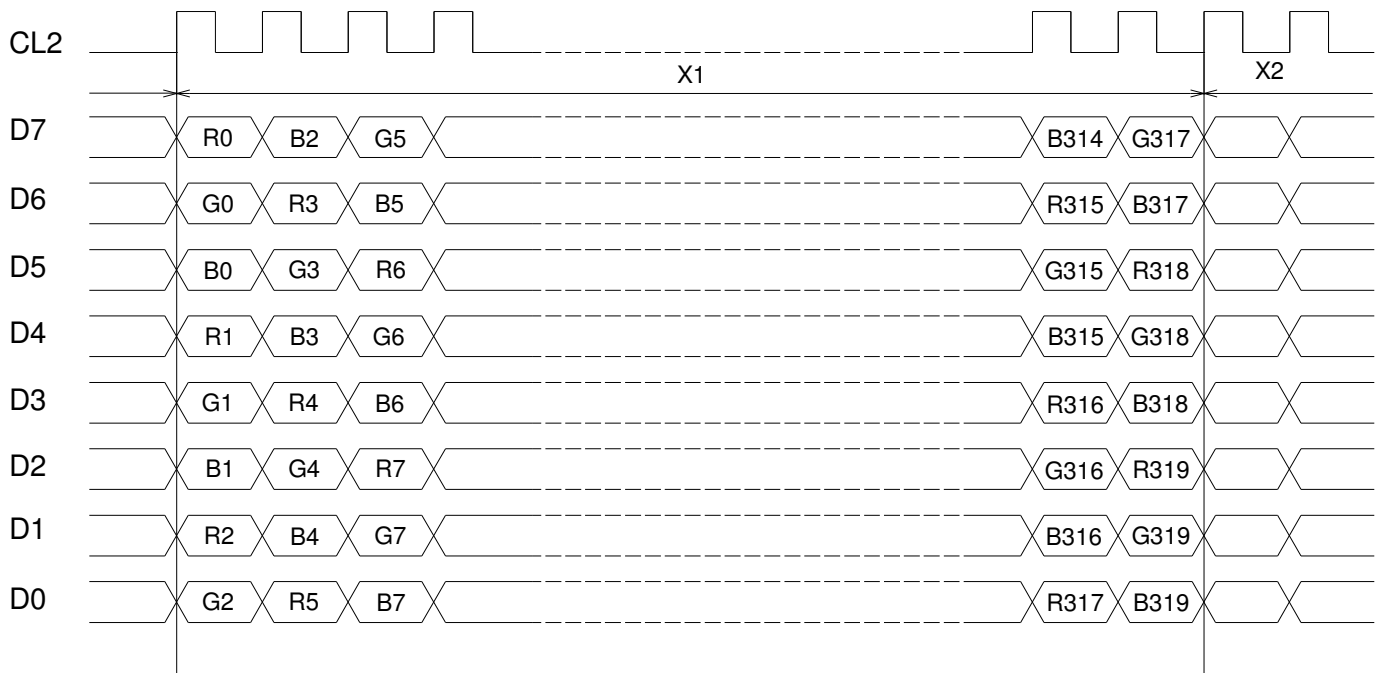
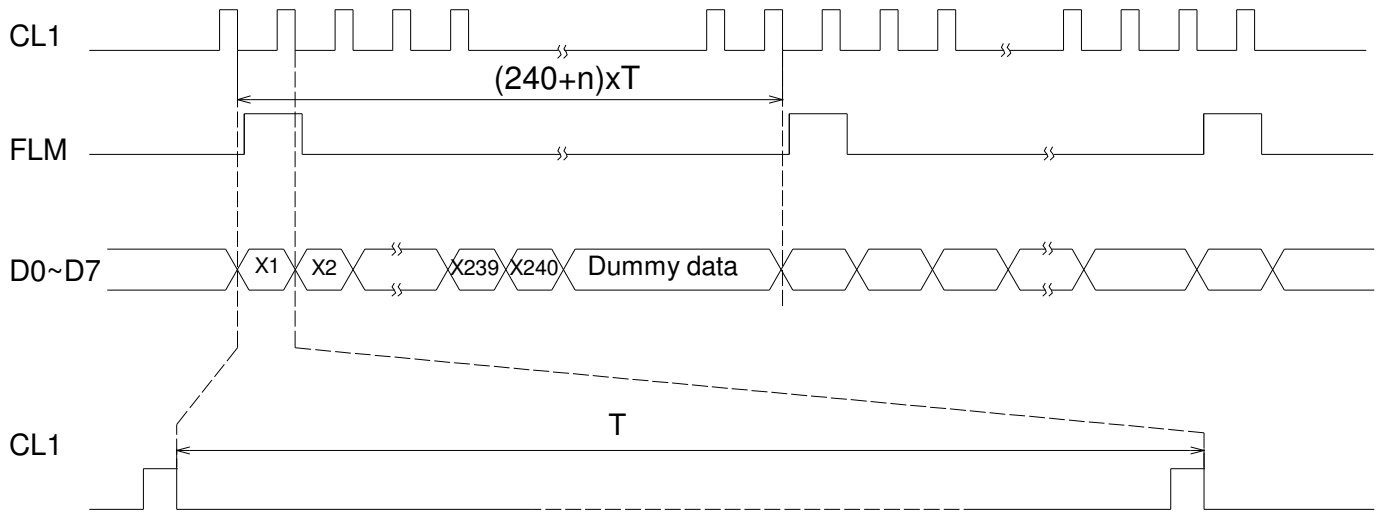
Note 4. Definition of the brightness Uniformity

$$\left( \frac{\text{Max brightness or Min brightness} - \text{Average brightness}}{\text{Average brightness}} \right) \times 100\%$$



# Interface Timing Chart

## Timing Chart



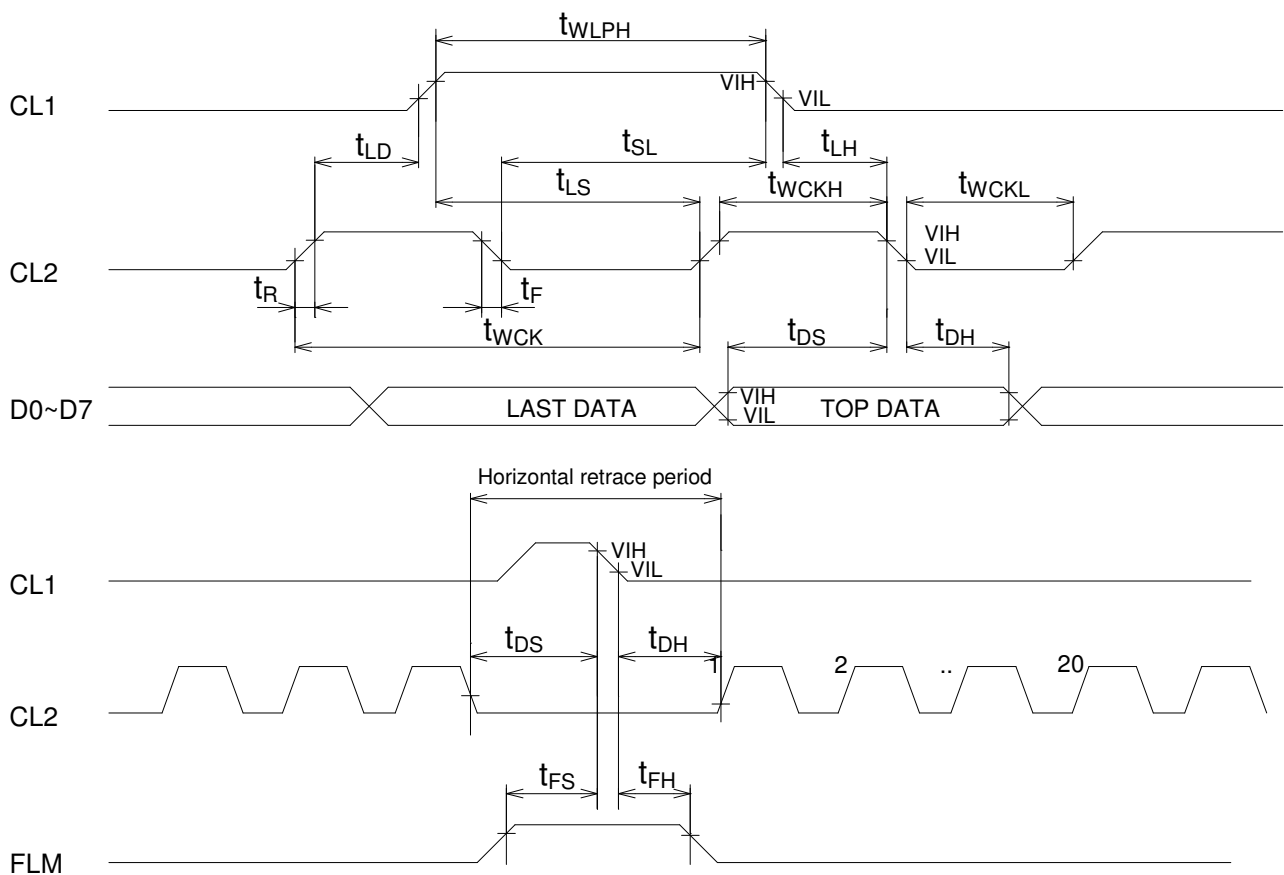
## Electrical Characteristics

(MODE1)

( $V_{DD}=3.0\sim 4.5V$ ,  $V_0=+10.0\sim +42.0V$ ,  $T_a=+0\sim 50\text{ }^\circ\text{C}$ )

Item	Symbol	Min.	Typ.	Max.	Unit
Shift clock period	$t_{WCK}$	66	-	-	ns
Shift clock "H" pulse wide	$t_{WCKH}$	23	-	-	ns
Shift clock "L" pulse wide	$t_{WCKL}$	23	-	-	ns
Data setup time	$t_{DS}$	10	-	-	ns
Data hold time	$t_{DH}$	25	-	-	ns
Latch pulse "H" pulse wide	$t_{WLPH}$	30	-	-	ns
Shift clock rise to latch pulse rise time	$t_{LD}$	10	-	-	ns
Shift clock fall to latch pulse fall time	$t_{SL}$	30	-	-	ns
Latch pulse rise to shift clock rise time	$t_{LS}$	30	-	-	ns
Latch pulse fall to shift clock fall time	$t_{LH}$	30	-	-	ns
Enable setup time	$t_{TS}$	12	-	-	ns
Input signal rise time	$t_R$	-	-	50	ns
Input signal fall time	$t_F$	-	-	50	ns
Output delay time	$t_D$	-	-	44	ns
FLM setup time	$t_{FS}$	30	-	-	ns
FLM hold time	$t_{FH}$	50	-	-	ns

Note 1:  $(t_{WCK} - t_{WCKH} - t_{WCKL})/2$  is maximum in the case of high speed operation



(MODE2)

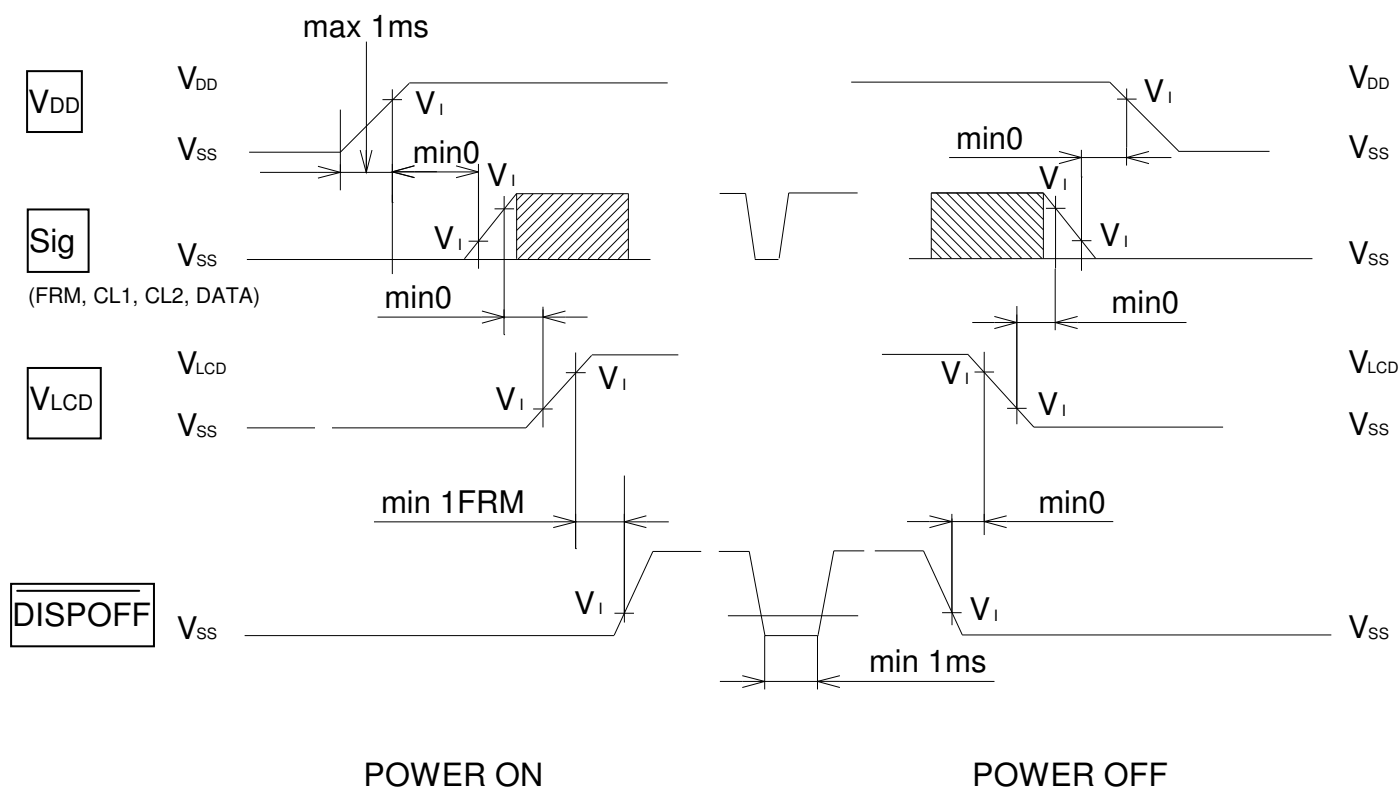
(V<sub>DD</sub>=4.5~5.5V , V<sub>0</sub>= +10.0~ +42.0V, Ta=+0°C~50°C)

Item	Symbol	Min.	Typ.	Max.	Unit
Shift clock period	t <sub>WCK</sub>	40	-	-	ns
Shift clock "H" pulse wide	t <sub>WCKH</sub>	12	-	-	ns
Shift clock "L" pulse wide	t <sub>WCKL</sub>	14	-	-	ns
Data setup time	t <sub>DS</sub>	5	-	-	ns
Data hold time	t <sub>DH</sub>	15	-	-	ns
Latch pulse "H" pulse wide	t <sub>WLPH</sub>	15	-	-	ns
Shift clock rise to latch pulse rise time	t <sub>LD</sub>	5	-	-	ns
Shift clock fall to latch pulse fall time	t <sub>SL</sub>	25	-	-	ns
Latch pulse rise to shift clock rise time	t <sub>LS</sub>	25	-	-	ns
Latch pulse fall to shift clock fall time	t <sub>LH</sub>	25	-	-	ns
Enable setup time	t <sub>TS</sub>	5	-	-	ns
Input signal rise time (Note 1)	t <sub>R</sub>	-	-	50	ns
Input signal fall time (Note 1)	t <sub>F</sub>	-	-	50	ns
Output delay time	t <sub>D</sub>	-	-	28	ns
FLM setup time	t <sub>FS</sub>	30	-	-	ns
FLM hold time	t <sub>FH</sub>	50	-	-	ns

Note 1: (t<sub>WCK</sub> - t<sub>WCKH</sub> - t<sub>WCKL</sub>)/2 is maximum in the case of high speed operation

## Power Supply and Signal Sequence

Do not apply DC voltage to the LCD panel because that induces the electrochemical reaction and reduces its life time. Please follow the power supply ON/OFF sequence to prevent DC driving of LCD or latch-up of COMS LSI, as shown below.



Note 1. Please keep the specified sequence because wrong sequence may cause permanent damage to the LCD panel.

Note 2. Please use  $\overline{DISPOFF}$  function. Switching by other than the  $\overline{DISPOFF}$  function may cause display deterioration.

Note 3.  $V_{LCD}$  voltage should be set up to adjusted voltage before  $\overline{DISPOFF}$  signal arises. Otherwise, when  $\overline{DISPOFF}$  signal arises, adjusted contrast image may not be generated.

Note 4. Please keep the specified sequence of  $\overline{DISPOFF}$  signal because if the signal is short enough, LCD panel may not be restarted. (min. 1ms)

**Input Data Allocation Table**

Data Signal	D 7	D 6	D 5	D 4	D 3	D 2	D 1	D 0	D 7	D 6	D 5	D 4		D 4	D 3	D 2	D 1	D 0
Y X	1	2	3	4	5	6	7	8	9	10	11	12	-----	9 5 6	9 5 7	9 5 8	9 5 9	9 5 0
1	R	G	B	R	G	B	R	G	B	R	G	B		G	B	R	G	B
2	R	G	B	R	G	B	R	G	B	R	G	B		G	B	R	G	B
3	R	G	B	R	G	B	R	G	B	R	G	B		G	B	R	G	B
4	R	G	B	R	G	B	R	G	B	R	G	B		G	B	R	G	B
5	R	G	B	R	G	B	R	G	B	R	G	B		G	B	R	G	B
6	R	G	B	R	G	B	R	G	B	R	G	B		G	B	R	G	B
7	R	G	B	R	G	B	R	G	B	R	G	B		G	B	R	G	B
8	R	G	B	R	G	B	R	G	B	R	G	B		G	B	R	G	B
9	R	G	B	R	G	B	R	G	B	R	G	B		G	B	R	G	B
10	R	G	B	R	G	B	R	G	B	R	G	B		G	B	R	G	B
238	R	G	B	R	G	B	R	G	B	R	G	B		G	B	R	G	B
239	R	G	B	R	G	B	R	G	B	R	G	B		G	B	R	G	B
240	R	G	B	R	G	B	R	G	B	R	G	B		G	B	R	G	B

R : RED  
 G : GREEN  
 B : BLUE

