INTEGRATED CIRCUITS-TTL

TRANSISTOR-TRANSISTOR LOGIC

(INCLUDES SERIES 74C CMOS)

NTE TYPE NO.	DESCRIPTION	PAGE NO.	DIAG NO.
7214	3-State Sel/Mlpx	1-280	249
7400	Quad 2-Input Pos NAND Gate	1-280	247
74C00	Quad 2-Input Pos NAND Gate	1-280	247
74H00	Quad 2-Input Pos NAND Gate	1-280	247
74LS00	Quad 2-Input Pos NAND Gate	1-280	247
74S00	Quad 2-Input Pos NAND Gate	1-280	247
7401	Quad 2-Input Pos NAND Gate with Open Collector Outputs	1-280	247
74H01	Quad 2-Input Pos NAND Gate with Open Collector Outputs	1-280	247
74LS01	Quad 2-Input NAND Gate with Open Collector Outputs	1-280	247
7402	Quad 2-Input Pos NOR Gate	1-280	247
74C02	Quad 2-Input Pos NOR Gate	1-280	247
74LS02	Quad 2-Input Pos NOR Gate	1-280	247
74S02	Quad 2-Input Pos NOR Gate	1-280	247
7403	Quad 2-Input Pos NAND Gate with Open Collector Outputs	1-280	247
74LS03	Quad 2-Input Pos NAND Gate with Open Collector Outputs	1-280	247
74S03	Quad 2-Input Pos NAND Gate with Open Collector Outputs	1-280	247
7404	Hex Inverter	1-280	247
74C04	Hex Inverter	1-280	247
74H04	Hex Inverter	1-280	247
74LS04	Hex Inverter	1-280	247
74S04	Hex Inverter	1-280	247
7405	Hex Inverter with Open Collector Outputs	1-280	247
74H05	Hex Inverter with Open Collector Outputs	1-280	247
74LS05	Hex Inverter with Open Collector Outputs	1-280	247
74S05	Hex Inverter with Open Collector Outputs	1-280	247
7406	Hex Inverter Buffer/Driver with Open Collector Hi Voltage Outputs	1-280	247
7407	Hex Buffer/Driver with Open Collector Hi Voltage Outputs	1-280	247
7408	Quad 2-Input Pos AND Gate	1-280	247
74C08	Quad 2-Input Pos AND Gate	1-280	247
74H08	Quad 2-Input Pos AND Gate	1-280	247
74LS08	Quad 2-Input Pos AND Gate	1-280	247
74S08	Quad 2-Input Pos AND Gate	1-280	247
7409	Quad 2-Input Pos AND Gate with Open Collector Outputs	1-280	247
74LS09	Quad 2-Input Pos AND Gate with Open Collector Outputs	1-280	247
74S09	Quad 2-Input Pos AND Gate with Open Collector Outputs	1-280	247
7410	Triple 3-Input Pos NAND Gate	1-281	247
74C10	Triple 3-Input Pos NAND Gate	1-281	247
74H10	Triple 3-Input Pos NAND Gate	1-281	247
74LS10	Triple 3-Input Pos NAND Gate	1-281	247
74S10	Triple 3-Input Pos NAND Gate	1-281	247
7411	Triple 3-Input Pos AND Gate	1-281	247
74H11	Triple 3-Input Pos AND Gate	1-281	247
74LS11	Triple 3-Input Pos AND Gate	1-281	247
74S11	Triple 3-Input Pos AND Gate	1-281	247
7412	Triple 3-Input Pos NAND Gate with Open Collector Outputs	1-281	247
74LS12	Triple 3-Input Pos NAND Gate with Open Collector Outputs	1-281	247
7413	Dual 4-Input NAND Schmitt Trigger	1-281	247
74LS13	Dual 4-Input NAND Schmitt Trigger	1-281	247
7414	Hex Schmitt Trigger	1-281	247
74C14	Hex Schmitt Trigger	1-281	247
74LS14	Hex Schmitt Trigger	1-281	247
74LS15	Triple 3-Input AND Gate with Open Collector Outputs	1-281	247
74S15	Triple 3-Input AND Gate with Open Collector Outputs	1-281	247
7416	Hex Inverter Buffer/Driver with Open Collector Hi Voltage Outputs	1-281	247
7417	Hex Buffer/Driver with Open Collector Hi Voltage Outputs	1-281	247
7420	Dual 4-Input NAND Gate	1-281	247
74C20	Dual 4-Input NAND Gate	1-281	247
74H20	Dual 4-Input NAND Gate	1-281	247
74LS20	Dual 4-Input NAND Gate	1-281	247
74S20	Dual 4-Input NAND Gate	1-281	247
7421	Dual 4-Input AND Gate	1-281	247
74H21	Dual 4-Input AND Gate	1-281	247
74LS21	Dual 4-Input AND Gate	1-281	247
7422	Dual 4-Input NAND Gate with Open Collector Outputs	1-281	247
74H22	Dual 4-Input NAND Gate with Open Collector Outputs	1-281	247
74LS22	Dual 4-Input NAND Gate with Open Collector Outputs	1-281	247
74S22	Dual 4-Input NAND Gate with Open Collector Outputs	1-281	247
7423	Expandable Dual 4-Input NOR Gate with Strobe	1-281	249
7425	Dual 4-Input NOR Gate with Strobe	1-282	247

NOTES:

C = The *74C* CMOS (Complementary Metal Oxide Silicon) series are a pin-for-pin functional equivalent to the 7400 TTL family. They have a wide power supply operating range, low power consumption, high noise immunity, and symmetric controlled rise and fall times.

H = The *74H* series is a line of high speed gates and flip-flops. These devices are used to substantially reduce critical path delay time.

LS = Abbreviation for *Low Power Schottky*, a type of TTL with a current and power reduction by a factor of 5 (compared to 7400 TTL), and an anti-saturation schottky diode.

S = The *74S* series is a line of super high speed devices. A type of TTL. They have a schottky-barrier diode clamping on all normally saturated devices.

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NTE TYPE NO.	DESCRIPTION	PAGE NO.	DIAG NO.
7426	Quad 2-Input Hi Voltage Interface NAND Gate	1-282	247
74LS26	Quad 2-Input Hi Voltage Interface NAND Gate	1-282	247
7427	Triple 3-Input Pos NOR Gate	1-282	247
74LS27	Triple 3-Input Pos NOR Gate	1-282	247
7428	Quad 2-Input Pos NOR Buffer/Clock Driver with Totem Pole Outputs	1-282	247
74LS28	Quad 2-Input Pos NOR Buffer/Clock Driver w/Totem Pole Outputs	1-282	247
7430	8-Input Pos NAND Gate	1-282	247
74C30	8-Input Pos NAND Gate	1-282	247
74H30	8-Input Pos NAND Gate	1-282	247
74LS30	8-Input Pos NAND Gate	1-282	247
74S30	8-Input Pos NAND Gate	1-282	247
7432	Quad 2-Input OR Gate	1-282	247
74C32	Quad 2-Input OR Gate	1-282	247
74LS32	Quad 2-Input OR Gate	1-282	247
7433	Quad 2-Input NOR Buffer with Open Collector Outputs	1-282	247
74LS33	Quad 2-Input NOR Buffer w/Open Collector Outputs	1-282	247
7437	Quad 2-Input NAND Buffer	1-282	247
74LS37	Quad 2-Input NAND Buffer	1-282	247
74S37	Quad 2-Input NAND Buffer	1-282	247
7438	Quad 2-Input NAND Buffer with Open Collector Outputs	1-282	247
74LS38	Quad 2-Input NAND Buffer with Open Collector Outputs	1-282	247
74S38	Quad 2-Input NAND Buffer with Open Collector Outputs	1-282	247
7439	Quad 2-Input NAND Buffer with Open Collector Output	1-282	247
7440	Dual 4-Input NAND Buffer	1-282	247
74H40	Dual 4-Input NAND Buffer	1-282	247
74LS40	Dual 4-Input NAND Buffer	1-282	247
74S40	Dual 4-Input NAND Buffer	1-282	247
7441	1-of-10 Decoder/Driver for Cold Cathode Indicator Tubes	1-282	249
7442	4-Line-to-10 Line BCD-to-Decimal Decoder	1-283	249
74C42	4-Line-to-10 Line BCD-to-Decimal Decoder	1-283	249
74LS42	4-Line-to-10 Line BCD-to-Decimal Decoder	1-283	249
7443	4-Line-to-10 Line Excess 3-to-Decimal Decoder	1-283	249
7444	4-Line-to-10 Line Excess 3-Gray-to-Decimal Decoder	1-283	249
7445	BCD-to-Decimal Decoder/Driver	1-283	249
7446	BCD-to-Seven-Segment Decoder/Driver with Active Low, Open Collector 30V Outputs	1-283	249
7447	BCD-to-Seven-Segment Decoder/Driver with Active Low, Open Collector 15V Outputs	1-283	249
74LS47	BCD-to-Seven-Segment Decoder/Driver with Active Low, Open Collector 15V Outputs	1-283	249
7448	BCD-to-Seven-Segment Decoder/Driver with Internal Pull-Up Outputs	1-283	249
74C48	BCD-to-Seven-Segment Decoder with Internal Pull-Up Outputs	1-283	249
74LS48	BCD-to-Seven-Segment Decoder/Driver with Internal Pull-Up Outputs	1-283	249
74LS49	BCD-to-Seven-Segment Decoder/Driver with Open Collector Outputs	1-283	247
7450	Dual 2-Wide 2-Input AND/OR Invert Gate (One-State Expandable)	1-283	247
74H50	Dual 2-Wide 2-Input AND/OR Invert Gate (One-State Expandable)	1-283	247
7451	Dual 2-Wide 2-Input AND/OR Invert Gate	1-283	247
74H51	Dual 2-Wide 2-Input AND/OR Invert Gate	1-283	247
74LS51	Dual 2-Wide 2-Input AND/OR Invert Gate	1-283	247
74S51	Dual 2-Wide 2-Input AND/OR Invert Gate	1-283	247
74H52	Expandable 4-Wide AND/OR Gate	1-283	247
7453	Expandable AND/OR Invert Gate	1-283	247
74H53	Expandable AND/OR Invert Gate	1-284	247
7454	4-Wide AND/OR Invert Gate	1-284	247
74H54	4-Wide AND/OR Invert Gate	1-284	247
74LS54	4-Wide AND/OR Invert Gate	1-284	247
74H55	Expandable 2-Wide 4-Input AND/OR Invert Gate	1-284	247
74LS55	2-Wide 4-Input AND/OR Invert Gate	1-284	247
7460	Dual 4-Input Expander	1-284	247
74H60	Dual 4-Input Expander	1-284	247
74H61	Triple 3-Input Expander	1-284	247
74H62	4-Wide AND/OR Expander	1-284	247
74LS63	Hex Current-Sensing Interface Gate	1-284	247
74S64	4-2-3-2 Input AND/OR Invert Gate with Totem Pole Outputs	1-284	247
74S65	4-2-3-2 Input AND/OR Invert Gate with Open Collector Outputs	1-284	247
7470	AND Gated J-K Pos Edge Triggered Flip-Flop with Preset and Clear	1-284	247
74H71	AND/OR Gated J-K Master Slave Flip-Flop with Preset	1-285	247
7472	AND Gated J-K M/S Flip-Flop with Preset and Clear	1-285	247

NOTES:

C = The 74*C* CMOS (Complementary Metal Oxide Silicon) series are a pin-for-pin functional equivalent to the 7400 TTL family. They have a wide power supply operating range, low power consumption, high noise immunity, and symmetric controlled rise and fall times.

H = The 74*H* series is a line of high speed gates and flip-flops. These devices are used to substantially reduce critical path delay time.

LS = Abbreviation for *L*ow *P*ower *S*chottky, a type of TTL with a current and power reduction by a factor of 5 (compared to 7400 TTL), and an anti- saturation schottky diode.

S = The 74*S* series is a line of super high speed devices. A type of TTL. They have a schottky-barrier diode clamping on all normally saturated devices.

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NTE TYPE NO.	DESCRIPTION	PAGE NO.	DIAG NO.
74H72	AND Gated J-K M/S Flip-Flop with Preset and Clear	1-285	247
7473	Dual J-K Flip-Flop with Clear	1-285	247
74C73	Dual J-K Flip Flop with Clear	1-285	247
74H73	Dual J-K Flip-Flop with Clear	1-285	247
74LS73	Dual J-K Flip-Flop with Clear	1-285	247
7474	Dual D-Type Pos Edge Triggered Flip-Flop with Preset and Clear	1-285	247
74C74	Dual D-Type Pos Edge Triggered Flip-Flop with Preset and Clear	1-285	247
74H74	Dual D-Type Pos Edge Triggered Flip-Flop with Preset and Clear	1-285	247
74LS74A	Dual D-Type Pos Edge Triggered Flip-Flop with Preset and Clear	1-285	247
74S74	Dual D-Type Pos Edge Triggered Flip-Flop with Preset and Clear	1-285	247
7475	4-Bit Bistable Latch	1-285	249
74LS75	4-Bit Bistable Latch	1-285	249
7476	Dual J-K Flip-Flop with Preset and Clear	1-285	249
74C76	Dual J-K Flip-Flop with Preset and Clear	1-285	249
74H76	Dual J-K Flip-Flop with Preset and Clear	1-285	249
74LS76A	Dual J-K Flip-Flop with Preset and Clear	1-285	249
74LS77	4-Bit Bistable Latch	1-285	247
74H78	Dual J-K Flip-Flop with Preset, Common Clear and Common Clock	1-285	247
74LS78	Dual J-K Flip-Flop with Preset, Common Clear and Common Clock	1-285	247
7480	Gated Full Adder	1-285	247
7481	16-Bit (16x1) Random Access Memory	1-285	247
7482	2-Bit Binary Full Adder	1-286	247
7483	4-Bit Binary Full Adder with Fast Carry	1-286	249
74LS83A	4-Bit Binary Full Adder with Fast Carry	1-286	249
7485	4-Bit Magnitude Comparator	1-286	249
74C85	4-Bit Magnitude Comparator	1-286	249
74LS85	4-Bit Magnitude Comparator	1-286	249
74S85	4-Bit Magnitude Comparator	1-286	249
7486	Quad 2-Input Exclusive OR Gate	1-286	247
74LS86	Quad 2-Input Exclusive OR Gate	1-286	247
74S86	Quad 2-Input Exclusive OR Gate	1-286	247
74H87	4-Bit True/Complement-Zero/One Element	1-286	247
7489	64-Bit Read/Write Memory	1-286	249
7490	Decade Counter Divide by 2 & 5	1-286	247
74C90	Decade Counter Divide by 2 & 5	1-286	247
74LS90	Decade Counter Divide by 2 & 5	1-286	247
7491	8-Bit Shift Register with Gated Serial Inputs and Serial Output	1-286	247
74LS91	8-Bit Shift Register with Gated Serial Inputs and Serial Output	1-286	247
7492	Divide-By-Twelve Counter	1-286	247
74LS92	Divide-By-Twelve Counter	1-286	247
7493A	4-Bit Binary Counter Divide by 2 & 8	1-287	247
74C93	4-Bit Binary Counter Divide by 2 & 8	1-287	247
74L93	4-Bit Binary Counter Divide by 2 & 8	1-287	247
74LS93	4-Bit Binary Counter Divide by 2 & 8	1-287	247
7494	4-Bit Shift Register Dual Asynchronous Presets	1-287	249
7495	4-Bit Parallel In/Parallel Out Shift Register	1-287	247
74C95	4-Bit Parallel In/Parallel Out Shift Register	1-287	247
74LS95B	4-Bit Parallel In/Parallel Out Shift Register	1-287	247
7496	5-Bit Shift Register w/Asynchronous Preset	1-287	249
7497	Synchronous 6-Bit Binary Rate Multiplier	1-287	249
74100	8-Bit Bistable Latch	1-287	252
74H101	AND/OR Gate J-K Negative Edge Triggered Flip-Flop with Preset	1-287	247
74H102	AND Gated J-K Negative Edge Triggered Flip-Flop with Clear and Reset	1-287	247
74H103	Dual J-K Negative Edge Triggered Flip-Flop with Clears	1-288	247
74105	J-K Master-Slave Flip-Flop	1-288	247
74H106	Dual J-K Negative Edge Triggered Flip-Flop with Presets and Clear	1-288	249
74107	Dual J-K Flip-Flop with Clear	1-288	247
74C107	Dual J-K Flip-Flop with Clear	1-288	247
74LS107	Dual J-K Flip-Flop with Clear	1-288	247
74H108	Dual J-K Negative Edge Triggered Flip-Flop w/ Presets, Common Clear & Clock	1-288	247
74109	Dual J-K Pos Edge Triggered Flip-Flop with Clear and Preset	1-288	249
74LS109A	Dual J-K Pos Edge Triggered Flip-Flop with Clear and Preset	1-288	249
74110	AND Gated J-K M/S Flip-Flop with Data Lockout	1-288	247
74111	Dual J-K Flip-Flop M/S with Data Lockout	1-288	249
74LS112A	Dual J-K Negative Edge Triggered Flip-Flop with Preset and Clear	1-288	249
74S112	Dual J-K Negative Edge Triggered Flip-Flop with Preset and Clear	1-288	249

NOTES:

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NTE TYPE NO.	DESCRIPTION	PAGE NO.	DIAG NO.
74LS113	Dual J-K Negative Edge Triggered Flip-Flop with Preset	1-288	247
74S113	Dual J-K Negative Edge Triggered Flip-Flop with Preset	1-288	247
74LS114	Dual J-K Negative Edge Triggered Flip-Flop with Preset, Common Clock and Clear	1-288	247
74S114	Dual J-K Negative Edge Triggered Flip-Flop with Preset, Common Clock and Clear	1-288	247
74116	Dual 4-Bit Latch	1-289	252
74120	Dual Pulse Synchronizer/Divider	1-289	249
74121	Monostable Multivibrators	1-289	247
74122	Retriggerable Monostable Multivibrators with Clear	1-289	247
74LS122	Retriggerable Monostable Multivibrators with Clear	1-289	247
74123	Dual Retriggerable Monostable Multivibrators with Clear	1-289	249
74LS123	Dual Retriggerable Monostable Multivibrators with Clear	1-289	249
74LS124	Dual Voltage Controlled Oscillator	1-289	249
74S124	Dual Voltage Controlled Oscillator	1-289	249
74125	Quad Bus Buffer with 3-State Outputs	1-289	247
74LS125A	Quad Bus Buffer with 3-State Outputs	1-289	247
74126	Quad Bus Buffer with 3-State Outputs	1-289	247
74LS126A	Quad Bus Buffer with 3-State Outputs	1-289	247
74128	Quad 2-Input NOR 50 Ohm-Line Driver	1-289	247
74132	Quad 2-Input Pos NAND Schmitt-Trigger	1-289	247
74LS132	Quad 2-Input Pos NAND Schmitt-Trigger	1-289	247
74S132	Quad 2-Input Pos NAND Schmitt-Trigger	1-289	247
74LS133	13-Input Pos NAND Gate	1-289	249
74S133	13-Input Pos NAND Gate	1-289	249
74S134	12-Input Positive-NAND Gate with 3-State Outputs	1-289	249
74136	Quad Exclusive-OR-Gate with Open Collector Outputs	1-290	247
74LS136	Quad Exclusive-OR-Gate with Open Collector Outputs	1-290	247
74LS138	3-to-8 Decoder/Demultiplexer	1-290	249
74S138	3-to-8 Decoder/Demultiplexer	1-290	249
74LS139	Dual 2-to-4 Line Decoder/Demultiplexer	1-290	249
74S140	Dual 4-Input Pos NAND 50 Ohm Line Driver	1-290	247
74141	BCD-to-Decimal Decoder/Driver-Drives Gas-Filled Cold Cathode Indicator Tubes Directly	1-290	247
74144	Counter/Latch/Decoder/Driver	1-290	249
74145	BCD-to-Decimal Decoder/Driver for Lamps, Relays, MOS	1-290	249
74LS145	BCD-to-Decimal Decoder/Driver for Lamps, Relays, MOS	1-290	249
74147	10-Line Decimal-to-4-Line BCD Priority Encoder	1-290	249
74LS147	10-Line Decimal-to-4-Line BCD Priority Encoder	1-290	249
74148	8-Line-to-3-Line Octal Priority Encoder	1-291	249
74LS148	8-Line-to-3-Line Octal Priority Encoder	1-291	249
74150	1-of-16 Data Selector/Multiplexer	1-291	252
74151	1-of-8 Data Selector/Multiplexer	1-291	249
74C151	1-of-8 Data Selector/Multiplexer	1-291	249
74LS151	1-of-8 Data Selector/Multiplexer	1-291	249
74S151	1-of-8 Data Selector/Multiplexer	1-291	249
74152	1-of-8 Data Selector/Multiplexer	1-291	247
74153	Dual 4-Line-to-1-Line Data Selector/Multiplexer	1-291	249
74LS153	Dual 4-Line-to-1-Line Data Selector/Multiplexer	1-291	249
74S153	Dual 4-Line-to-1-Line Data Selector/Multiplexer	1-291	249
74154	4-Line-to-16-Line Decoder/Demultiplexer	1-291	252
74C154	4-Line-to-16-Line Decoder/Demultiplexer	1-291	252
74155	Dual 2-Line-to-4-Line Decoder/Demultiplexer with Totem Pole Outputs	1-291	249
74LS155	Dual 2-Line-to-4-Line Decoder/Demultiplexer with Totem Pole Outputs	1-291	249
74156	Dual 2-Line-to-4-Line Decoder/Demultiplexer with Open Collector Outputs	1-291	249
74LS156	Dual 2-Line-to-4-Line Decoder/Demultiplexer with Open Collector Outputs	1-291	249
74157	Quad 2-to-1-Line Data Selector/Multiplexer with Non Inverted Data Outputs	1-291	249
74C157	Quad 2-to-1-Line Data Selector/Multiplexer with Non Inverted Data Outputs	1-291	249
74LS157	Quad 2-to-1-Line Data Selector/Multiplexer with Non Inverted Data Outputs	1-291	249
74S157	Quad 2-to-1-Line Data Selector/Multiplexer with Non Inverted Outputs	1-291	249
74158	Quad 2-to-1-Line Data Selector/Multiplexer with Inverted Data Outputs	1-291	249
74LS158	Quad 2-to-1-Line Data Selector/Multiplexer with Inverted Data Outputs	1-291	249
74S158	Quad 2-to-1-Line Data Selector/Multiplexer with Inverted Data Outputs	1-291	249

NOTES:

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NTE TYPE NO.	DESCRIPTION	PAGE NO.	DIAG NO.
74160	Synchronous 4-Bit Decade Counter with Direct Clear	1-292	249
74C160	Synchronous 4-Bit Decade Counter with Direct Clear	1-292	249
74LS160A	Synchronous 4-Bit Decade Counter with Direct Clear	1-292	249
74161	Synchronous 4-Bit Bindary Counter with Direct Clear	1-292	249
74C161	Synchronous 4-Bit Binary Counter with Direct Clear	1-292	249
74LS161A	Synchronous 4-Bit Binary Counter with Direct Clear	1-292	249
74162	Synchronous 4-Bit Decade Counter w/Clear	1-292	249
74LS162A	Synchronous 4-Bit Decade Counter w/Clear		
74163	Synchronous 4-Bit Binary Counter w/Clear	1-292	249
74LS163A	Synchronous 4-Bit Binary Counter w/Clear	1-292	249
74S163	Synchronous 4-Bit Binary Counter w/Clear	1-292	249
74164	8-Bit Parallel-Out Serial Shift Register with Asynchronous Clear	1-292	247
74C164	8-Bit Parallel-Out Serial Shift Register with Asynchronous Clear	1-292	247
74LS164	8-Bit Parallel-Out Serial Shift Register with Asynchronous Clear	1-292	247
74165	Parallel-Load 8-Bit Shift Register with Complementary Outputs	1-292	249
74LS165	Parallel-Load 8-Bit Shift Register with Complementary Outputs	1-292	249
74166	8-Bit Parallel or Serial In-Serial Out Shift Register	1-292	249
74LS166	8-Bit Parallel or Serial In-Serial Out Shift Register	1-292	249
74LS166A	8-Bit Shift Register	1-292	249
74LS168A	4-Stage Up/Down, BCD Decade Counter	1-292	249
74LS169A	4-State, Up/Down, Modulo 16 Binary Ctr	1-292	249
74170	4-By-4 Register File, Open Collector Out	1-293	249
74LS170	4-By-4 Register File, Open Collector Out	1-293	249
74173	4-Bit D-Type Flip-Flop with 3-State Outputs	1-293	249
74C173	4-Bit D-Type Flip-Flop with 3-State Outputs	1-293	249
74LS173A	4-Bit D-Type Flip-Flop with 3-State Outputs	1-293	249
74174	Hex D-Type Flip-Flop with Serial Rail Outputs and Common Direct Clear	1-293	249
74C174	Hex D-Type Flip-Flop with Serial Rail Outputs and Common Direct Clear	1-293	249
74LS174	Hex D-Type Flip-Flop with Serial Rail Outputs and Common Direct Clear	1-293	249
74S174	Hex D-Type Flip-Flop with Serial Rail Outputs and Common Direct Clear	1-293	249
74175	Quad D-Type Flip-Flop with Common Direct Clear and Complementary Outputs	1-293	249
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S = The 74*S* series is a line of super high speed devices. A type of TTL. They have a schottky-barrier diode clamping on all normally saturated devices.

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74C240	Octal Buffer/Line Driver/Receiver with 3-State Inverted Outputs	1-295	294
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74LS249	BCD-to-Seven-Segment Decoder/Driver with Open Collector Outputs	1-296	249
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74S258	Quad Data Selector/Multiplexer with Inverted 3-State Outputs	1-297	249
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74S260	Dual 5-Input NOR Gate	1-297	247
74265	Quad Complementary-Output Elements	1-297	249
74LS266	Quad 2-Input Exclusive NOR Gate with Open Collector Outputs	1-297	247
74LS273	Octal D-Type Flip-Flop with Common Clock and Single Rail Outputs	1-297	294
74278	4-Bit Cascadable Priority Register with Latched Data Inputs and Priority Output Gating	1-297	247
74279	Quad Set-Reset Latch with Diode-Clamped Inputs and Totem Pole Outputs	1-297	249
74LS279	Quad Set-Reset Latch with Diode-Clamped Inputs and Totem Pole Outputs	1-297	249
74LS280	9-Bit ODD/EVEN Parity Generator/Checker (N-Bit Cascadable)	1-297	247
74S280	9-Bit ODD/EVEN Parity Generator/Checker (N-Bit Cascadable)	1-297	247
74LS283	4-Bit Binary Full Adder	1-297	249
74S287	1K PROM, 3-State Outputs	1-297	249
74S288	256-Bit PROM, 3-State Open Collector	1-298	249
74290	Decade Counter (Divide by 2 & 5)	1-298	247
74LS290	Decade Counter (Divide by 2 & 5)	1-298	247
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74LS364	Octal "D" Flip-Flop with 3-State Outputs	1-299	294
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74LS365A	Hex Buffer/Driver with Non Inverted 3-State Outputs and Gated Enable Inputs	1-299	249
74366	Hex Bus Drivers with Inverted 3-State Output and Gated Enable Inputs	1-299	249
74LS366A	Hex Bus Drivers with Inverted 3-State Output and Gated Enable Inputs	1-299	249
74367	Hex Buffer Driver with Non Inverted 3-State Outputs, Organized to Handle 4-Bit Data	1-299	249
74LS367	Hex Buffer Driver with Non Inverted 3-State Outputs, Organized to Handle 4-Bit Data	1-299	249
74368	Hex Bus Driver with Non Inverted 3-State Outputs, Organized to Handle 4-Bit Data	1-299	249
74LS368	Hex Bus Driver with Non Inverted 3-State Outputs, Organized to Handle 4-Bit Data	1-299	249
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74LS373	Octal D-Type Latch with 3-State Outputs and Common Output Control	1-299	294
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74S374	Octal D-Type Flip-Flop with 3-State Outputs, Common Output Control and Common Clock	1-299	294
74376	Quad J-K Flip-Flop	1-299	249
74LS377	Octal D-Type Flip-Flop with Common Enable with Clock and Single Rail Outputs	1-299	294
74LS378	Hex "D" Flip-Flop with Clock Enable	1-299	249
74LS379	Quad "D" Type Flip-Flop	1-300	249
74LS386	Quad 2-Input Exclusive OR Gate	1-300	247
74390	Dual Decade Ripple Counter	1-300	249
74LS390	Dual Decade Ripple Counter	1-300	249
74393	Dual 4-Bit Binary Ripple Counter	1-300	247
74LS393	Dual 4-Bit Binary Ripple Counter	1-300	247
74LS395A	4-Bit Cascadable Shift Register with 3-State Outputs	1-300	249
74LS396	Octal Storage Register with Parallel Access	1-300	249
74LS398	Quad 2-Port Register	1-300	294
74LS399	Quad 2-Input Multiplexer with Storage	1-300	249
74426	Quad Gate with 3-State Outputs and Active-High Enabling	1-300	247
74LS445	BCD-to-Decimal Decoder/Driver with Open Collectors	1-301	249
74S472	4K PROM with 3-State Outputs	1-301	294
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74LS645	Octal Bus Transceiver with 3-State Outputs	1-302	294
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74C901	Hex Inverting TTL Buffer	1-302	247

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74C903	Hex Inverting CMOS Buffer	1-302	247
74C904	Hex Non-Inverting CMOS Buffer	1-302	247
74C922	16-Key Keyboard Encoder with 3-State Output	1-302	298
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74C925	4-Digit Counter with Multiplexed 7-Segment Output Driver	1-302	249
80C95	Hex 3-State Buffer with Common Enable	1-303	249
80C96	Hex 3-State Inverter with Common Enable	1-303	249
80C97	3-State Hex Buffer	1-303	249
8123	Quad 2-Input Data Selector/Multiplexer	1-303	249
8223	1-Line-to-8-Line Demultiplexer	1-303	249
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8309	Data Selector/Multiplexer	1-303	249
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8316	Synchronous 4-Bit Binary Counter	1-303	249
8318	8-Input Priority Encoder	1-303	249
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8374	7-Segment Latch/Decoder/Driver for Common Anode LED	1-304	249
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8542	3-State Quad I/O Register	1-304	249
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8556	3-State Programmable Binary Counter	1-304	249
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96LS02	Dual Retriggerable/Resettable Monostable Multivibrator	1-305	249
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