

MATERIAL NUMBER	# OF COLUMNS	# OF DIFF PAIR	DIM 'A' MAX	DIM 'B'	PTH ϕ
76500-#8**	8	32	15.20	13.30	0.46+0.05
76500-#1**	10	40	19.00	17.10	0.46+0.05
76500-#6**	16	64	30.40	28.50	0.46+0.05
76500-#8**	8	32	15.20	13.30	0.39+0.05
76500-#1**	10	40	19.00	17.10	0.39+0.05
76500-#6**	16	64	30.40	28.50	0.39+0.05

76500-****

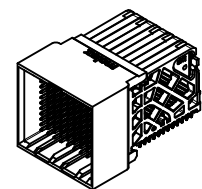
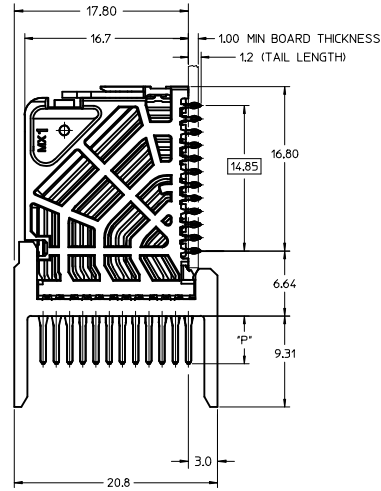
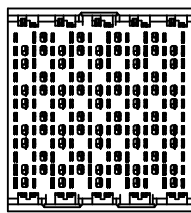
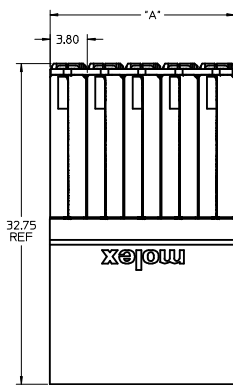
MODULE TYPE
1 = UNGUIDED, LEAD-FREE

OF COLUMNS
1 = 10 COLUMNS
6 = 16 COLUMNS
8 = 8 COLUMNS

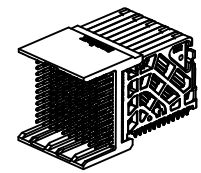
PIN LENGTH (P)
4 = 4.90 (0.46 PTH)
5 = 5.50 (0.46 PTH)
7 = 4.90 (0.39 PTH)
8 = 5.50 (0.39 PTH)

END WALL OPTIONS
0 = OPEN ENDS
1 = LEFT ENDWALL
2 = DUAL ENDWALL
3 = RIGHT ENDWALL

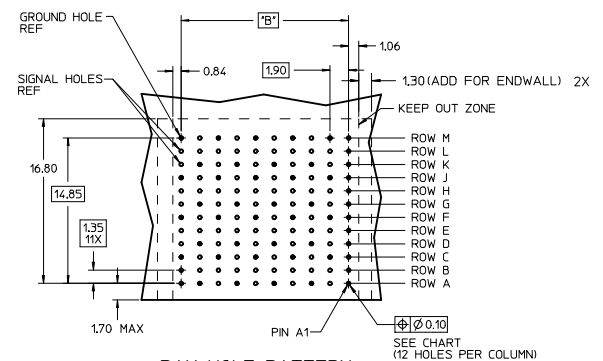
- NOTES:
1. MATERIALS: HOUSING - LIQUID CRYSTAL POLYMER (LCP), GLASS-FILLED, UL94V-0
TERMINALS - HIGH PERFORMANCE COPPER ALLOY
 2. FINISH: 30NM SELECTIVE GOLD IN CONTACT AREA, SELECTIVE TIN ON PCB TAILS, NICKEL OVERALL.
 3. REFER TO MOLEX PRODUCT SPECIFICATION PS-76060-999 FOR PERFORMANCE SPECIFICATIONS AND ADDITIONAL PCB INFORMATION.
 4. EACH SIGNAL WAFER CONTAINS 2 COLUMNS OF TERMINALS.
 5. PRODUCT IS PACKAGED PER PK-70873-041.
 6. THIS PART CONFORMS TO CLASS B REQUIREMENTS OF COSMETIC SPEC PS-45499-002.
 7. REFER TO MOLEX SALES DRAWING SD-76160-001 FOR THE MATING RECEPTABLES.
 8. REFER TO MOLEX PCB ROUTING GUIDE AS-76060-990 FOR ANTIPAD AND ROUTING RECOMMENDATIONS.



DUAL WALL OPTION

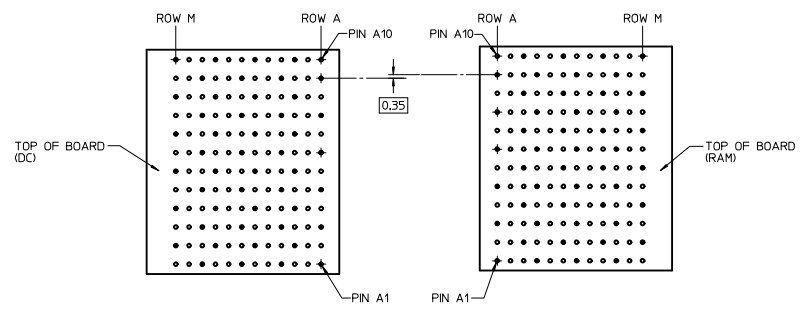
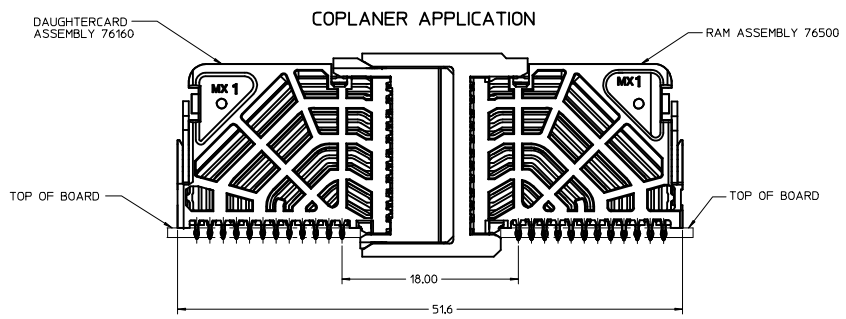


OPEN END OPTION



RAM HOLE PATTERN (CONNECTOR SIDE)

STANDARDIZE IEC NO: LCP2013-5157 DRAWN BY: MILLER CHYK: 2013/05/30 APPR: TEL 2013/06/06	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)	DIMENSION STYLE	SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION
	$\nabla=0$	4 PLACES \pm --- \pm INCH	MM ONLY	4:1	METRIC	
	$\nabla=0$	3 PLACES \pm --- \pm .005				
	$\nabla=0$	2 PLACES \pm 0.13 \pm .010				
		1 PLACE \pm 0.25 \pm ---				
		0 PLACE \pm --- \pm ---				
		ANGULAR \pm 1/2°				
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS				
			MATERIAL NO.	DOCUMENT NO.		
			SEE TABLE	SD-76500-001		
						SHEET NO. 1 OF 2



DC PIN A10-----RAM PIN A10
 DC PIN A1-----RAM PIN A1

SEE SHEET 1 IEC NO: UCP2013-5157 DRAWN BY MILLER 2013/05/20 CHKD: APPR:TELO 2013/06/06 REV	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)	DIMENSION STYLE	SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION
	▽=0	mm	MM ONLY	4:1	METRIC	
	▽=0	INCH				
	▽=0	4 PLACES ±--- ±---	DRAWN BY	DATE	TITLE	
	▽=0	3 PLACES ±--- ±.005	JLAURX	2008/11/24	IMPACT 4 PAIR RAM UNGUIDED SIGNAL MODULE SALES DRAWING	
	2 PLACES ±.13 ±.010	CHECKED BY	DATE	DOCUMENT NO.		
	1 PLACE ±0.25 ±---	JLAURX	2008/11/25	SD-76500-001		
	0 PLACE ±--- ±---	APPROVED BY	DATE	SHEET NO.		
	ANGULAR ±1/2°	JLAURX	2008/11/26	2 OF 2		
	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	MATERIAL NO.	SEE TABLE	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION		